

Volume 1 Issue 12, November 2013

International Journal of Inventive

Engineering and Sciences

ISSN : 2319-9598

website: www.ijies.org



Blue Eyes Intelligence Engineering & Sciences Publication Pvt. Ltd.
Exploring Innovation: A Key for Dedicated Services

Address:

22, First Floor, ShivLoka Phase-IV,
Khajuri Kala, BHEL-Piplani, Bhopal (M.P.)-462021, India

Website: www.blueeyesintelligence.org

Email: director@blueeyesintelligence.org, blueeyes@gmail.com

Cell #: +91-9669981618. WhatsApp #: +91-9669981618. Viber #: +91-9669981618

Skype #: beiesp. Twitter #: beiesp

Editor In Chief

Dr. Shiv K Sahu

Ph.D. (CSE), M.Tech. (IT, Honors), B.Tech. (IT)

Director, Blue Eyes Intelligence Engineering & Sciences Publication Pvt. Ltd., Bhopal(M.P.), India

Dr. Shachi Sahu

Ph.D. (Chemistry), M.Sc. (Organic Chemistry)

Additional Director, Blue Eyes Intelligence Engineering & Sciences Publication Pvt. Ltd., Bhopal(M.P.), India

Vice Editor In Chief

Dr. Himani Sharma

Professor & Dean, Department of Electronics & Communication Engineering, MLR Institute of Technology, Laxman Reddy Avenue, Dundigal, Hyderabad, India

Prof.(Dr.) Anuranjan Misra

Professor & Head, Computer Science & Engineering and Information Technology & Engineering, Noida International University, Noida (U.P.), India

Chief Advisory Board

Prof. (Dr.) Hamid Saremi

Vice Chancellor of Islamic Azad University of Iran, Quchan Branch, Quchan-Iran

Dr. Uma Shanker

Professor & Head, Department of Mathematics, CEC, Bilaspur(C.G.), India

Dr. Rama Shanker

Professor & Head, Department of Statistics, Eritrea Institute of Technology, Asmara, Eritrea

Dr. Vinita Kumari

Blue Eyes Intelligence Engineering & Sciences Publication Pvt. Ltd., India

Dr. Kapil Kumar Bansal

Head (Research and Publication), SRM University, Gaziabad (U.P.), India

Dr. Deepak Garg

Professor, Department of Computer Science and Engineering, Thapar University, Patiala (Punjab), India, Senior Member of IEEE, Secretary of IEEE Computer Society (Delhi Section), Life Member of Computer Society of India (CSI), Indian Society of Technical Education (ISTE), Indian Science Congress Association Kolkata.

Dr. Vijay Anant Athavale

Director of SVS Group of Institutions, Mawana, Meerut (U.P.) India/ U.P. Technical University, India

Dr. T.C. Manjunath

Principal & Professor, HKBK College of Engg, Nagawara, Arabic College Road, Bengaluru-560045, Karnataka, India

Dr. Kosta Yogeshwar Prasad

Director, Technical Campus, Marwadi Education Foundation's Group of Institutions, Rajkot-Morbi Highway, Gauridada, Rajkot, Gujarat, India

Dr. Dinesh Varshney

Director of College Development Counseling, Devi Ahilya University, Indore (M.P.), Professor, School of Physics, Devi Ahilya University, Indore (M.P.), and Regional Director, Madhya Pradesh Bhoj (Open) University, Indore (M.P.), India

Dr. P. Dananjayan

Professor, Department of Department of ECE, Pondicherry Engineering College, Pondicherry, India

Dr. Sadhana Vishwakarma

Associate Professor, Department of Engineering Chemistry, Technocrat Institute of Technology, Bhopal(M.P.), India

Dr. Kamal Mehta

Associate Professor, Deptment of Computer Engineering, Institute of Technology, NIRMA University, Ahmedabad (Gujarat), India

Dr. CheeFai Tan

Faculty of Mechanical Engineering, University Technical, Malaysia Melaka, Malaysia

Dr. Suresh Babu Perli

Professor & Head, Department of Electrical and Electronic Engineering, Narasaraopeta Engineering College, Guntur, A.P., INDIA

Dr. Binod Kumar

Associate Professor, School of Engineering and Computer Technology, Faculty of Integrative Sciences and Technology, Quest International University, Ipoh, Perak, Malaysia

Dr. Chiladze George

Professor, Faculty of Law, Akhaltsikhe State University, Tbilisi University, Georgia

Dr. Kavita Khare

Professor, Department of Electronics & Communication Engineering., MANIT, Bhopal (M.P.), INDIA

Dr. C. Saravanan

Associate Professor (System Manager) & Head, Computer Center, NIT, Durgapur, W.B. India

Dr. S. Saravanan

Professor, Department of Electrical and Electronics Engineering, Muthayamal Engineering College, Resipuram, Tamilnadu, India

Dr. Amit Kumar Garg

Professor & Head, Department of Electronics and Communication Engineering, Maharishi Markandeshwar University, Mullana, Ambala (Haryana), India

Dr. T.C.Manjunath

Principal & Professor, HKBK College of Engg, Nagawara, Arabic College Road, Bengaluru-560045, Karnataka, India

Dr. P. Dananjayan

Professor, Department of Department of ECE, Pondicherry Engineering College, Pondicherry, India

Dr. Kamal K Mehta

Associate Professor, Department of Computer Engineering, Institute of Technology, NIRMA University, Ahmedabad (Gujarat), India

Dr. Rajiv Srivastava

Director, Department of Computer Science & Engineering, Sagar Institute of Research & Technology, Bhopal (M.P.), India

Dr. Chakunta Venkata Guru Rao

Professor, Department of Computer Science & Engineering, SR Engineering College, Ananthasagar, Warangal, Andhra Pradesh, India

Dr. Anuranjan Misra

Professor, Department of Computer Science & Engineering, Bhagwant Institute of Technology, NH-24, Jindal Nagar, Ghaziabad, India

Dr. Robert Brian Smith

International Development Assistance Consultant, Department of AEC Consultants Pty Ltd, AEC Consultants Pty Ltd, Macquarie Centre, North Ryde, New South Wales, Australia

Dr. Saber Mohamed Abd-Allah

Associate Professor, Department of Biochemistry, Shanghai Institute of Biochemistry and Cell Biology, Yue Yang Road, Shanghai, China

Dr. Himani Sharma

Professor & Dean, Department of Electronics & Communication Engineering, MLR Institute of Technology, Laxman Reddy Avenue, Dundigal, Hyderabad, India

Dr. Sahab Singh

Associate Professor, Department of Management Studies, Dronacharya Group of Institutions, Knowledge Park-III, Greater Noida, India

Dr. Umesh Kumar

Principal: Govt Women Poly, Ranchi, India

Dr. Syed Zaheer Hasan

Scientist-G Petroleum Research Wing, Gujarat Energy Research and Management Institute, Energy Building, Pandit Deendayal Petroleum University Campus, Raisan, Gandhinagar-382007, Gujarat, India.

Dr. Jaswant Singh Bhomrah

Director, Department of Profit Oriented Technique, 1 – B Crystal Gold, Vijalpore Road, Navsari 396445, Gujarat. India

Technical Advisory Board

Dr. Mohd. Husain

Director, MG Institute of Management & Technology, Banthara, Lucknow (U.P.), India

Dr. T. Jayanthi

Principal, Panimalar Institute of Technology, Chennai (TN), India

Dr. Umesh A.S.

Director, Technocrats Institute of Technology & Science, Bhopal(M.P.), India

Dr. B. Kanagasabapathi

Infosys Labs, Infosys Limited, Center for Advance Modeling and Simulation, Infosys Labs, Infosys Limited, Electronics City, Bangalore, India

Dr. C.B. Gupta

Professor, Department of Mathematics, Birla Institute of Technology & Sciences, Pilani (Rajasthan), India

Dr. Sunandan Bhunia

Associate Professor & Head, Dept. of Electronics & Communication Engineering, Haldia Institute of Technology, Haldia, West Bengal, India

Dr. Jaydeb Bhaumik

Associate Professor, Dept. of Electronics & Communication Engineering, Haldia Institute of Technology, Haldia, West Bengal, India

Dr. Rajesh Das

Associate Professor, School of Applied Sciences, Haldia Institute of Technology, Haldia, West Bengal, India

Dr. Mrutyunjaya Panda

Professor & Head, Department of EEE, Gandhi Institute for Technological Development, Bhubaneswar, Odisha, India

Dr. Mohd. Nazri Ismail

Associate Professor, Department of System and Networking, University of Kuala (UniKL), Kuala Lumpur, Malaysia

Dr. Haw Su Cheng

Faculty of Information Technology, Multimedia University (MMU), Jalan Multimedia, 63100 Cyberjaya

Dr. Hossein Rajabalipour Cheshmehgaz

Industrial Modeling and Computing Department, Faculty of Computer Science and Information Systems, Universiti Teknologi Malaysia (UTM) 81310, Skudai, Malaysia

Dr. Sudhinder Singh Chowhan

Associate Professor, Institute of Management and Computer Science, NIMS University, Jaipur (Rajasthan), India

Dr. Neeta Sharma

Professor & Head, Department of Communication Skills, Technocrat Institute of Technology, Bhopal(M.P.), India

Dr. Ashish Rastogi

Associate Professor, Department of CSIT, Guru Ghansi Das University, Bilaspur (C.G.), India

Dr. Santosh Kumar Nanda

Professor, Department of Computer Science and Engineering, Eastern Academy of Science and Technology (EAST), Khurda (Orisa), India

Dr. Hai Shanker Hota

Associate Professor, Department of CSIT, Guru Ghansi Das University, Bilaspur (C.G.), India

Dr. Sunil Kumar Singla

Professor, Department of Electrical and Instrumentation Engineering, Thapar University, Patiala (Punjab), India

Dr. A. K. Verma

Professor, Department of Computer Science and Engineering, Thapar University, Patiala (Punjab), India

Dr. Durgesh Mishra

Chairman, IEEE Computer Society Chapter Bombay Section, Chairman IEEE MP Subsection, Professor & Dean (R&D), Acropolis Institute of Technology, Indore (M.P.), India

Dr. Xiaoguang Yue

Associate Professor, College of Computer and Information, Southwest Forestry University, Kunming (Yunnan), China

Dr. Veronica Mc Gowan

Associate Professor, Department of Computer and Business Information Systems, Delaware Valley College, Doylestown, PA, Allman China

Dr. Mohd. Ali Hussain

Professor, Department of Computer Science and Engineering, Sri Sai Madhavi Institute of Science & Technology, Rajahmundry (A.P.), India

Dr. Mohd. Nazri Ismail

Professor, System and Networking Department, Jalan Sultan Ismail, Kuala Lumpur, MALAYSIA

Dr. Sunil Mishra

Associate Professor, Department of Communication Skills (English), Dronacharya College of Engineering, Farrukhnagar, Gurgaon (Haryana), India

Dr. Labib Francis Gergis Rofaiel

Associate Professor, Department of Digital Communications and Electronics, Misr Academy for Engineering and Technology, Mansoura City, Egypt

Dr. Pavol Tanuska

Associate Professor, Department of Applied Informatics, Automation, and Mathematics, Trnava, Slovakia

Dr. VS Giridhar Akula

Professor, Avanthi's Research & Technological Academy, Gunthapally, Hyderabad, Andhra Pradesh, India

Dr. S. Satyanarayana

Associate Professor, Department of Computer Science and Engineering, KL University, Guntur, Andhra Pradesh, India

Dr. Bhupendra Kumar Sharma

Associate Professor, Department of Mathematics, KL University, BITS, Pilani, India

Dr. Praveen Agarwal

Associate Professor & Head, Department of Mathematics, Anand International College of Engineering, Jaipur (Rajasthan), India

Dr. Manoj Kumar

Professor, Department of Mathematics, Rashtriya Kishan Post Graduate Degree, College, Shamli, Prabudh Nagar, (U.P.), India

Dr. Shaikh Abdul Hannan

Associate Professor, Department of Computer Science, Vivekanand Arts Sardar Dalipsing Arts and Science College, Aurangabad (Maharashtra), India

Dr. K.M. Pandey

Professor, Department of Mechanical Engineering, National Institute of Technology, Silchar, India

Prof. Pranav Parashar

Technical Advisor, International Journal of Soft Computing and Engineering (IJSCE), Bhopal (M.P.), India

Dr. Biswajit Chakraborty

MECON Limited, Research and Development Division (A Govt. of India Enterprise), Ranchi-834002, Jharkhand, India

Dr. D.V. Ashoka

Professor & Head, Department of Information Science & Engineering, SJB Institute of Technology, Kengeri, Bangalore, India

Dr. Sasidhar Babu Suvanam

Professor & Academic Coordinator, Department of Computer Science & Engineering, Sree Narayana Gurukulam College of Engineering, Kadayiuruppu, Kolenchery, Kerala, India

Dr. C. Venkatesh

Professor & Dean, Faculty of Engineering, EBET Group of Institutions, Kangayam, Erode, Caimbatore (Tamil Nadu), India

Dr. Nilay Khare

Assoc. Professor & Head, Department of Computer Science, MANIT, Bhopal (M.P.), India

Dr. Sandra De Iaco

Professor, Dip.to Di Scienze Dell'Economia-Sez. Matematico-Statistica, Italy

Dr. Yaduvir Singh

Associate Professor, Department of Computer Science & Engineering, Ideal Institute of Technology, Govindpuram Ghaziabad, Lucknow (U.P.), India

Dr. Angela Amphawan

Head of Optical Technology, School of Computing, School Of Computing, Universiti Utara Malaysia, 06010 Sintok, Kedah, Malaysia

Dr. Ashwini Kumar Arya

Associate Professor, Department of Electronics & Communication Engineering, Faculty of Engineering and Technology, Graphic Era University, Dehradun (U.K.), India

Dr. Yash Pal Singh

Professor, Department of Electronics & Communication Engg, Director, KLS Institute Of Engg.& Technology, Director, KLSIET, Chandok, Bijnor, (U.P.), India

Dr. Ashish Jain

Associate Professor, Department of Computer Science & Engineering, Accurate Institute of Management & Technology, Gr. Noida (U.P.), India

Dr. Abhay Saxena

Associate Professor&Head, Department. of Computer Science, Dev Sanskriti University, Haridwar, Utrakhand, India

Dr. Judy. M.V

Associate Professor, Head of the Department CS &IT, Amrita School of Arts and Sciences, Amrita Vishwa Vidyapeetham, Brahmasthanam, Edapally, Cochin, Kerala, India

Dr. Sangkyun Kim

Professor, Department of Industrial Engineering, Kangwon National University, Hyoja 2 dong, Chunche0nsi, Gangwondo, Korea

Dr. Sanjay M. Gulhane

Professor, Department of Electronics & Telecommunication Engineering, Jawaharlal Darda Institute of Engineering & Technology, Yavatmal, Maharastra, India

Dr. K.K. Thyagarajan

Principal & Professor, Department of Informational Technology, RMK College of Engineering & Technology, RSM Nagar, Thiruyallur, Tamil Nadu, India

Dr. P. Subashini

Assoc. Professor, Department of Computer Science, Coimbatore, India

Dr. G. Srinivasrao

Professor, Department of Mechanical Engineering, RVR & JC, College of Engineering, Chowdavaram, Guntur, India

Dr. Rajesh Verma

Professor, Department of Computer Science & Engg. and Deptt. of Information Technology, Kurukshetra Institute of Technology & Management, Bhor Sadian, Pehowa, Kurukshetra (Haryana), India

Dr. Pawan Kumar Shukla

Associate Professor, Satya College of Engineering & Technology, Haryana, India

Dr. U C Srivastava

Associate Professor, Department of Applied Physics, Amity Institute of Applied Sciences, Amity University, Noida, India

Dr. Reena Dadhich

Prof. & Head, Department of Computer Science and Informatics, MBS MArg, Near Kabir Circle, University of Kota, Rajasthan, India

Dr. Aashis. S. Roy

Department of Materials Engineering, Indian Institute of Science, Bangalore Karnataka, India

Dr. Sudhir Nigam

Professor Department of Civil Engineering, Principal, Lakshmi Narain College of Technology and Science, Raisen, Road, Bhopal, (M.P.), India

Dr. S. Senthil Kumar

Doctorate, Department of Center for Advanced Image and Information Technology, Division of Computer Science and Engineering, Graduate School of Electronics and Information Engineering, Chon Buk National University Deok Jin-Dong, Jeonju, Chon Buk, 561-756, South Korea Tamilnadu, India

Dr. Gufran Ahmad Ansari

Associate Professor, Department of Information Technology, College of Computer, Qassim University, Al-Qassim, Kingdom of Saudi Arabia (KSA)

Dr. R. Navaneetha krishnan

Associate Professor, Department of MCA, Bharathiyar College of Engg & Tech, Karaikal Puducherry, India

Dr. Hossein Rajabalipour Cheshmejjaz

Industrial Modeling and Computing Department, Faculty of Computer Science and Information Systems, Universiti Teknologi Skudai, Malaysia

Dr. Veronica McGowan

Associate Professor, Department of Computer and Business Information Systems, Delaware Valley College, Doylestown, PA, Allman China

Dr. Sanjay Sharma

Associate Professor, Department of Mathematics, Bhilai Institute of Technology, Durg, Chhattisgarh, India

Dr. Taghreed Hashim Al-Noor

Professor, Department of Chemistry, Ibn-Al-Haitham Education for pure Science College, University of Baghdad, Iraq

Dr. Madhumita Dash

Professor, Department of Electronics & Telecommunication, Orissa Engineering College, Bhubaneswar, Odisha, India

Dr. Anita Sagadevan Ethiraj

Associate Professor, Department of Centre for Nanotechnology Research (CNR), School of Electronics Engineering (Sense), Vellore Institute of Technology (VIT) University, Tamilnadu, India

Dr. Sibasis Acharya

Project Consultant, Department of Metallurgy & Mineral Processing, Midas Tech International, 30 Mukin Street, Jindalee-4074, Queensland, Australia

Dr. Neelam Ruhil

Professor, Department of Electronics & Computer Engineering, Dronacharya College of Engineering, Gurgaon, Haryana, India

Dr. Faizullah Mahar

Professor, Department of Electrical Engineering, Balochistan University of Engineering and Technology, Pakistan

Dr. K. Selvaraju

Head, PG & Research, Department of Physics, Kandaswami Kandars College (Govt. Aided), Velur (PO), Namakkal DT. Tamil Nadu, India

Dr. M. K. Bhanarkar

Associate Professor, Department of Electronics, Shivaji University, Kolhapur, Maharashtra, India

Dr. Sanjay Hari Sawant

Professor, Department of Mechanical Engineering, Dr. J. J. Magdum College of Engineering, Jaysingpur, India

Dr. Arindam Ghosal

Professor, Department of Mechanical Engineering, Dronacharya Group of Institutions, B-27, Part-III, Knowledge Park, Greater Noida, India

Dr. M. Chithirai Pon Selvan

Associate Professor, Department of Mechanical Engineering, School of Engineering & Information Technology Manipal University, Dubai, UAE

Dr. S. Sambhu Prasad

Professor & Principal, Department of Mechanical Engineering, Pragati College of Engineering, Andhra Pradesh, India.

Dr. Muhammad Attique Khan Shahid

Professor of Physics & Chairman, Department of Physics, Advisor (SAAP) at Government Post Graduate College of Science, Faisalabad.

Dr. Kuldeep Pareta

Professor & Head, Department of Remote Sensing/GIS & NRM, B-30 Kailash Colony, New Delhi 110 048, India

Dr. Th. Kiranbala Devi

Associate Professor, Department of Civil Engineering, Manipur Institute of Technology, Takyelpat, Imphal, Manipur, India

Dr. Nirmala Mungamuru

Associate Professor, Department of Computing, School of Engineering, Adama Science and Technology University, Ethiopia

Dr. Srilalitha Giriya Kumari Sagi

Associate Professor, Department of Management, Gandhi Institute of Technology and Management, India

Dr. Vishnu Narayan Mishra

Associate Professor, Department of Mathematics, Sardar Vallabhbhai National Institute of Technology, Ichchhanath Mahadev Dumas Road, Surat (Gujarat), India

Dr. Yash Pal Singh

Director/Principal, Somany (P.G.) Institute of Technology & Management, Garhi Bolni Road , Rewari Haryana, India.

Dr. Sripada Rama Sree

Vice Principal, Associate Professor, Department of Computer Science and Engineering, Aditya Engineering College, Surampalem, Andhra Pradesh. India.

Dr. Rustom Mamlook

Associate Professor, Department of Electrical and Computer Engineering, Dhofar University, Salalah, Oman. Middle East.

Managing Editor

Mr. Jitendra Kumar Sen

International Journal of Advanced Engineering and Nano Technology (IJAENT)

Editorial Board

Dr. Vikas Maheshwari

Associate Professor, Department of Electrical Communication Engineering, Amity University Madhya-Pradesh Gwalior, M.P., India

Dr. Sudhakara A

Associate Professor, Department of Chemistry, Jain Institute of Technology Davanagere, Karnataka, India

Dr. Jammi Ashok

Associate Professor, Department of Electrical and Computer Engineering, Hawassa University, Hawassa.(East Africa)

Dr. Mohamed Ashabrawy

Associate Professor, Department of Computer Science, Salman bin Abdulaziz University Kingdom, Saudi Arabia

Dr. Omer Muhammad Ayoub

Associate Professor, Department of Computer Science, Punjab University Affected Center Abdullah Sulayman Road, Al-Fayyaz, Jeddah, KSA Saudi Arabia

Dr. M. Seenivasan

Associate Professor, Department of Mathematics, Annamalai University Annamalaiagar, Tamil Nadu, India

Dr. S.V.G.V.A. Prasad

Associate Professor, Department of Physics, Ideal College of Arts & Sciences, Kakinada, A.P, India.

Dr. S. Omkumar

Associate Professor, Department of Electronics and Communication Engineering, SCSVMV University, Enathur, Kanchipuram – 631 561. Tamilnadu, India.

Dr. Yousef FARHAOUI

Associate Professor, Department of Computer Science, Faculty of Sciences and Technic, Moulay Ismail University, B.P 509, Boutalamine, Errachidia, Morocco.

Dr. Gutta Sridevi

Associate Professor, Department of Computer Science & Engineering, K L University, Vaddeswaram, Guntur (DT) Andhra Pradesh. India.

Dr. Debmalya Bhattacharya

Associate Professor, Department of Electronics & Communication Engineering, University of Technology & Management, Bawri Mansion, Dhankheti, Shillong-793003, Meghalaya, India.

Dr. K. Harinadha Reddy

Associate Professor, Department of Electrical and Electronics Engineering, L B R College of Engineering, Mylavaram, Krishna District, Andhra Pradesh State - 5 21 230, India.

Dr. C. Gajendran

Associate Professor, Department of Civil Engineering, School of Civil Engineering, Karunya Nagar, Karunya University, Coimbatore – 641114, Tamil Nadu, India.

Dr. Dibya Prakash Rai

Assistant Professor, Department of Physics, College of Aizawl, Pachhunga University, Mizoram, India.

Dr. Sreenivasa Reddy

Associate Professor, Department of Chemistry, Sri Krishnadevaraya University, Anantapur-515003, A.P., India.

Dr. P. K. Dhal

Associate Professor, Department of Electrical and Electronics Engineering, Vel Tech, Dr. RR & Dr. SR Technical University, Chennai, India.

Dr. M. A. Ashabrawy

Associate Professor, Department of Computer Science, Atomic Energy Authority, Salman bin Abdulaziz University, Al Kharj Saudi Arabia.

Dr. K. Meenakshi Sundaram

Professor & Head, Department of Computer Science, Agnel Institute of Technology and Design, Assagao - Bardez, Goa. India.

Dr. Persis Voola

Associate Professor, Department of Computer Science and Engineering, Adikavi Nannaya University, Rajah Narendra Nagar, Rajahmundry-533296 Andhra Pradesh, India.

Dr. Abhijit Banerjee

Associate Professor, Department of Electronics and Instrumentation Engineering, Academy of Technology, Hooghly, Grand Trunk Rd, Adisaptagram, Aedconagar, West Bengal, India.

Dr. D. Amaranatha Reddy

Associate Professor, Department of Chemistry, Pusan National University, Busan, South Korea.

Dr. A. Heidari

Associate Professor, Department of Chemistry, Postdoctoral Research Fellow, California South University (CSU), Irvine, California, USA

Dr. Ashwani Kumar Aggarwal

Assistant Professor, Department of Electrical and Instrumentation Engineering, Sant Longowal Institute of Engineering and Technology, Longowal, Punjab, India.

Dr. P. Srinivas

Assistant Professor, Department of Electrical Engineering, University College of Engineering Osmania University, Hyderabad-500007, Telangana, India.

Dr. Sandeep Chettri

DST-SERB, Young Scientist, Department of Physics, Mizoram University, Tanhril, Aizawl, Mizoram 796004, India.

Dr. Elsanosy M. Elamin

Assistant Professor, Department of Electrical and Electronic Engineering, Faculty of Engineering, University of Kordofan B.O.Box: 160 Elobeid, (Sudan). North Africa.

Dr. Porag Kalita

Professor & Head, Department of Automobile Engineering, Jorhat, Assam, India.

Dr. T. A. Ashok Kumar

Associate Professor, Department of Computer Science, Christ University, Bengaluru, Karnataka, India.

Dr. Malini M Patil

Associate Professor, Department of Information Science and Engineering, JSS Academy of Technical Education, JSS Campus, Bangalore-560060, Karnataka, India.

Dr. V. Selvan

Associate Professor, Department of Civil Engineering, Sri Ramakrishna Engineering College, Vattamalaipalayam, Coimbatore, Tamil Nadu, India.

Dr. Syed Umar

Associate Professor, Department of Computer Science and Engineering, Koneru Lakshmaiah University, Vaddeswaram, Guntur, Andhra Pradesh, India.

S. No	Volume-1 Issue-12, November 2013, ISSN: 2319-9598 (Online) Published By: Blue Eyes Intelligence Engineering & Sciences Publication Pvt. Ltd.		Page No.	
1.	Authors:	Neha Thombare, Pallavi Deshmukh, Simantini Patil, Shailesh Jain		
	Paper Title:	Personalized Image Search		
	<p>Abstract: Personalized Search is a feature in which when a user is logged into a account, all of his or her searches on Personal Search are recorded into Web History. Then, when a user performs a search, the search results are not only based on the relevancy of each web page to the search term, but the service also takes into account what websites the user previously visited through search results to determine which search results to determine for future searches, to provide a more personalized experience. The feature only takes effect after the user has performed several searches, so that it can be calibrated to the user's tastes. Social sharing websites like facebook, twitter, YouTube they are allowing user to comment, tag, like and unlike the shared documents or images. Rapid Increase in the search services for social websites has been developed.</p> <p>Keywords: Personalized Search, Tagging, Topic Model.</p> <p>References:</p> <ol style="list-style-type: none"> 1. Learn to Personalized Image Search from the Photo Sharing Websites Jitao Sang, Changsheng Xu, Senior Member, IEEE, Dongyuan Lu 2. B. Smyth, "A community-based approach to personalizing web search," Computer, vol. 40, no. 8, pp. 42–50, 2007. 3. Personalized Search on Flickr based on Searcher's Preference Prediction Dongyuan Lu, Quidan Li 		1-3	
Authors:	Y.Shekar, B. Vasunayak, J. Sunil Kumar, A. Sanyasi Rao, Fathima Shireen			
Paper Title:	Cryptographic Algorithms Implementation on RISC Processor			
2.	<p>Abstract: Security is one of the most important features in data communication. Cryptographic algorithms are mainly used for this purpose to obtain confidentiality and integrity of data in communication. Implementing a cryptographic algorithm on a general purpose processor it results lower throughput and larger power consumption. In this work we propose processor architecture to perform the cryptographic algorithms and also it speed up the encryption and decryption process of data. This processor will perform the cryptographic operations as like general instructions in GPP. The data size of this processor is 32-bit. The architecture of the processor designed using Verilog HDL.</p> <p>Keywords: Cryptographic Algorithms, GPP, Verilog.</p> <p>References:</p> <ol style="list-style-type: none"> 1. Jun-hong chen "A High-Performance Unified Field Reconfigurable Cryptographic Processor". IEEE-2010 2. Nima Karimpour Darav "CIARP: Crypto Instruction-aware RISC Processor.IEEE-2012" 3. Antonio H. Zavala "RISC-Based Architecture for Computer Hardware Instruction" IEEE-2011 4. "Data Encryption Standard" 1999 october 25. 5. "Advance Encryption Standard" November 26 2001 6. Imyong Lee, Dongwook Lee, Kiyoung choi, "ODALRISC: A Small, Low power and Configurable 32-bit RISC processor," International SOC design Conference 2008. 7. Wayne Wolf, FPGA Based System Design, Prentice Hall, 2005. 8. National Institute of Standards and Technology (NIST), "Advanced Encryption Standard (AES), (FIPUB 197)", November 26, 2001, http://csrc.nist.gov/publications/. 9. Atri. Rudra, Pradeep k. Dubey, Charanjit S.Jutla, Vijay Kumar, Josyula R.Rao, Pankaj Rahotgi, "Efficient Implementation of Rijndael Encryption with Composite Field Arithmetic," Proceedings of Cryptographic Hardware and Embedded Systems (CHES), Vol. 2162, pp.175-188, 2001. 10. Rohit Sharma, Vivek Kumar Sehgal, Nitin Nitin1, Pranav Bhasker, Ishita Verma, "Design and Implementation of 64-Bit RISC Processor using Computer Modeling and Simulation," Proceedings of UKsim, Vol. 11, pp. 568 – 573, 2009. 11. R. Uma, "Design and performance analysis of 8-bit RISC Processor using Xilinx tool," International Journal of Engineering Research and Applications (IJERA), Vol. 2, Issue 2, pp.53-58, March-April 2012, ISSN: 2248-9622. 12. Jean-Luc Beuchat, "FPGA Implementations of the RC6 Block Cipher," Laboratoire de l'Informatique du arall'elisme, Ecole Normale Sup'erieure de Lyon,46, All'ee d'Italie, F-69364 Lyon Cedex 07. 13. Arturo Diaz-Perez, Nazar A. Saqib, Francisco Rodriguez-Henriquez, "Implementing Symmetric-Key Cryptosystems on Reconfigurable-Hardware," springer Nov 2006, ISBN : 0387338837. 14. Imyong Lee, Dongwook Lee, Kiyoung choi, "ODALRISC: A Small, Low power and Configurable 32-bit RISC processor," International SOC design Conference 2008. 15. R. Razdan and M.D. Smith, "A High-Performance Micro architecture with Hardware-Programmable Functional Units," Proceedings of. Micro-27, pp. 172-180, 1994. 16. Vincent P. Heuring, and Harry F. Jordan, "Computer Systems Design and Architecture," Second Edition, 6th Dec, 2003, ISBN-10: 0130484407. 17. Dave Van den Bout "The Practical XILINX Designer Lab Book," pp.30-31, ISBN 0-13- 095502-7. 18. XILINX datasheet library, http://www.xilinx.com/partinfo/4000.pdf 19. Jonas Thor, "Evaluation of a reconfigurable computing engine for digital communication Applications," pp.12-17, ISSN 1402-1617. 20. Rasset T.L, Niederland R.A, Lane J.H, Geideman W.A "A 32-b RISC Implemented in Enhancement-Mode JFET Ga As," Vol.3, pp.60-70, 9 Oct 1986. 21. Dolle.M, Jhand. S, Lehner.W, Muller.O, Schlett.M. "A 32-b RISC/DSP microprocessor with reduced complexity," Proceedings of journals and magazines, Vol. 32, Issue 7, pp 1056-1066, 06 August 2002. 22. Buhler, M. Baitinger "VHDL-based development of a 32-bit pipelined RISC processor," U.G. Stuttgart University, Vol 1, pp. 138-142, 06 August 2002. 		4-8	
	Authors:	Geethanjali Marri, P. Sri Padma, Ch. Ganapathi Reddy		
	Paper Title:	Conjugate Gradient Based MMSE Filter for Uplink Orthogonal Frequency Division Multiple Access Systems		

3.	<p>Abstract: Carrier Frequency Offset (CFO) compensation is very important for reliable detection of transmitted data in uplink Orthogonal Frequency Division Multiple Access (OFDMA) systems. In this paper we proposed a low-complexity CFO compensation algorithm based on the Minimum Mean Square Error (MMSE) criterion for uplink OFDMA systems. The proposed algorithm employs a Conjugate Gradient (CG) method which iteratively finds the MMSE solution. In this paper we are presenting the proposed method by comparing with the existing direct MMSE method and we show that CFO can be compensated with substantially reduced computational complexity by applying the CG method.</p> <p>Keywords: Carrier Frequency Offset (CFO), Orthogonal Frequency Division Multiple Access (OFDMA), Conjugate Gradient (CG).</p> <p>References:</p> <ol style="list-style-type: none"> 1. Cao, Z, Tureli, U, Yu-Dong Yao and Honan. P, "Frequency synchronization for generalized OFDMA uplink," 2004 IEEE GLOBECOM Proc, Vol. 2, pp. 1071-1075. 2. "IEEE standard for local and metropolitan area networks, part 16: air interface for fixed and mobile broadband wireless access systems amendment 2: physical and medium access control layers for combined fixed and mobile operation in licensed bands," IEEE Std. 802.16e, Feb. 2006. 3. J. R. Shewchuk, An Introduction to the Conjugate-Gradient Method Without the Agonizing Pain. Carnegie Mellon University, School of Computer Science, 1994. 4. K. Etemad, "Overview of mobile WiMAX technology and evolution," IEEE Commun. Mag., vol. 46, pp. 31-40, Oct. 2008. 5. Kilbom Lee, Sang-Rim Lee, Sung-Hyun Moon and Inkyu Lee, "MMSE-based CFO compensation for uplink OFDMA systems with conjugate gradient," IEEE Trans. Wireless Commun., vol.11, AUG 2012, pp. 2767-2775. 6. Michele Moreli, Jay Kuo. C, "Synchronization techniques for orthogonal frequency division multiple access," IEEE Proc. Vol.95, no.7, July 2007, pp.1394-1427. 7. Samuel C.Yang, "OFDMA system analysis and design," pp.1-92. 8. Sreedhar. D and Chockalingam. A, "MMSE receiver for multiuser interference cancellation in uplink OFDMA," IEEE Trans. Wireless Commun., vol.6, Nov. 2008, pp. 2125-2129. 	9-15				
4.	<table border="1"> <tr> <td>Authors:</td> <td>Sreenivasa Reddy Mula</td> </tr> <tr> <td>Paper Title:</td> <td>Power Factor Correction</td> </tr> </table> <p>Abstract: In this paper, a new parallel-connected single phase power factor correction (PFC) topology using flyback converter in parallel with forward converter is proposed to improve the input power factor with simultaneously output voltage regulation taking consideration of current harmonic norms. Paralleling of converter modules is a well-known technique that is often used in medium-power applications to achieve the desired output power by using smaller size of high frequency transformers and inductors. The proposed approach offers cost effective, compact and efficient AC-DC converter by the use of parallel power processing. Forward converter primarily regulates output voltage with fast dynamic response and it acts as master which processes 60% of the power. Flyback converter with AC/DC PFC stage regulates input current shaping and PFC, and processes the remaining 40% of the power as a slave. A parallel-connected interleaved structure offers smaller passive components, less loss even in continuous conduction inductor current mode, and reduced volt-ampere rating of DC/DC stage converter. MATLAB/SIMULINK is used for implementation and simulation results show the performance improvement.</p> <p>Keywords: Ac-dc converter, pwm, pfc, simulink, matlab.</p> <p>References:</p> <ol style="list-style-type: none"> 1. R. Redl: "Power-factor correction in single-phase switching-mode power supplies-an overview", Int. J. Electronics, Vol. 77, No. 5, pp. 555-582, 1994. 2. P. Tonti, G. Spiazzi: "Harmonic Limiting Standards and Power Factor Correction Techniques". Tutorial presented at the European Power Electronics Conference (EPE), Sevilla (Spain), September 1995. 3. R. Redl, L Balogh : "RMS, DC, Peak and Harmonic Currents in High- Frequency Power-Factor Correctors with Capacitive Energy Storage". Proceeding of APEC '92, pp. 533-540. 4. J. Zhang, M.M. Jovanovic, F.C. Lee: "Comparison between CCM single-stage and two-stage boost PFC converters". Proceeding of APEC '99, pp.335-41. 5. Chow, M.H.L.; Siu, K.W.; Tse, C.K.; Yim-Shu Lee: "A novel method for elimination of line-current harmonics in single-stage PFC switching regulators". IEEE Transactions on Power Electronics, vol.13, (no.1), Jan. 1998. pp.75-83. 	Authors:	Sreenivasa Reddy Mula	Paper Title:	Power Factor Correction	16-17
Authors:	Sreenivasa Reddy Mula					
Paper Title:	Power Factor Correction					
	<table border="1"> <tr> <td>Authors:</td> <td>Raghava Yathiraju</td> </tr> <tr> <td>Paper Title:</td> <td>Acoustic Echo Cancellation Using Conventional Adaptive Algorithms</td> </tr> </table> <p>Abstract: An adaptive filter is a filter that self-adjusts its transfer function according to an optimization algorithm driven by an error signal. Because of the complexity of the optimization algorithms, most adaptive filters are digital filters. Adaptive filtering constitutes one of the core technologies in digital signal processing and finds numerous application areas in science as well as in industry. Adaptive filtering techniques are used in a wide range of applications, including, adaptive noise cancellation, echo cancellation, adaptive equalization and adaptive beamforming. Acoustic echo cancellation is a common occurrence in today's telecommunication systems. The signal interference caused by acoustic echo is distracting to users and causes a reduction in the quality of the communication. This paper focuses on the use of Least Mean Square (LMS), Normalised Least Mean Square (NLMS), Variable Step-Size Least Mean Square (VSLMS), Variable Step-Size Normalised Least Mean Square (VSNLMS) and Recursive Least Square (RLS) algorithms to reduce this unwanted echo, thus increasing communication quality.</p> <p>Keywords: Adaptive filters, Echo, Adaptive algorithms, Echo cancellation, Acoustic echo cancellation.</p> <p>References:</p> <ol style="list-style-type: none"> 1. Homana, I; Topa, M.D.; Kirei, B.S.; "Echo cancelling using adaptive algorithms", Design and Technology of Electronics Packages, 	Authors:	Raghava Yathiraju	Paper Title:	Acoustic Echo Cancellation Using Conventional Adaptive Algorithms	
Authors:	Raghava Yathiraju					
Paper Title:	Acoustic Echo Cancellation Using Conventional Adaptive Algorithms					

5.	<p>(SIITME) 15th International Symposium., pp. 317-321, Sept.2009.</p> <ol style="list-style-type: none"> 2. Eneman, K.; Moonen, M.; "Iterated partitioned block frequency-domain adaptive filtering for acoustic echo cancellation," IEEE Transactions on Speech and Audio Processing, vol. 11, pp. 143-158, March 2003. 3. G. Egelmeers, P. Sommen, and J. de Boer, "Realization of an acoustic echo canceller on a single DSP," in Proc. Eur. Signal Processing Conf. (EUSIPCO96), Trieste, Italy, pp. 33-36, Sept. 1996. 4. Soria, E.; Calpe, J.; Chambers, J.; Martinez, M.; Camps, G.; Guerrero, J.D.M.; "A novel approach to introducing adaptive filters based on the LMS algorithm and its variants", IEEE Transactions, vol. 47, pp. 127-133, Feb 2008. 5. Krishna, E.H.; Raghuram, M.; Madhav, K.V; Reddy, K.A; "Acoustic echo cancellation using a computationally efficient transform domain LMS adaptive filter," 2010 10th International Conference on Information sciences signal processing and their applications (ISSPA), pp. 409-412, May 2010. 6. E. Soria, J. Calpe, J. Guerrero, M. Martínez, and J. Espí, "An easy demonstration of the optimum value of the adaptation constant in the LMS algorithm," IEEE Trans. Educ., vol. 41, pp. 83, Feb. 1998. 7. D. Morgan and S. Kratzer, "On a class of computationally efficient rapidly converging, generalized NLMS algorithms," IEEE Signal Processing Lett., vol. 3, pp. 245-247, Aug. 1996 8. Tandon, A.; Ahmad, M.O.; Swamy, M.N.S.; "An efficient, low-complexity, normalized LMS algorithm for echo cancellation", IEEE workshop on Circuits and Systems, 2004. NEWCAS 2004, pp. 161-164, June 2004. 9. Lee, K.A.; Gan, W.S; "Improving convergence of the NLMS algorithm using constrained subband updates," Signal Processing Letters IEEE, vol. 11, pp. 736-739, Sept. 2004. 10. D.L. Duttweiler, "Proportionate Normalized Least Mean Square Adaptation in Echo Cancellers," IEEE Trans. Speech Audio Processing, vol. 8, pp. 508-518, Sept. 2000. 11. Sriesti, P.; Lu, W.-S.; Antoniou, A.; "A new variable step-size LMS algorithm and its application in subband adaptive filtering for echo cancellation," The 2001 IEEE International Symposium on Circuits and Systems, 2001. ISCAS 2001, vol. 2, pp. 721-724, May 2001. 12. Tingchan, W.; Chutchavong, V.; Benjangkprasert, C.; " Performance of A Robust Variable Step-Step LMS Adaptive Algorithm for multiple Echo Cancellation in Telephone Network," SICE-ICASE, 2006. International Joint Conference, pp. 3173-3176, Oct 2006. 13. Li Yan; Wang Xinan; "A Modified VSLMS Algorithm," The 9th International Conference on Advanced Communication Technology, vol. 1, pp. 615-618, Feb 2007. 14. J. B. Evans, P. Xue, and B. Liu, "Analysis and implementation of variable step size adaptive algorithms," IEEE Trans. Signal Processing, vol. 41, pp. 2517- 2535, Aug. 1993. 15. Paleologu, C.; Benesty, J.; Grant, S.L.; Osterwise, C.; "Variable step-size NLMS algorithms for echo cancellation" 2009 Conference Record of the forty-third Asilomar Conference on Signals, Systems and Computers., pp. 633-637, Nov 2009. 16. Ahmed I. Sulyman and Azzedine Zerguine, "Echo Cancellation Using a Variable Step-Size NLMS Algorithm", Electrical and Computer Engineering Department Queen's University. 17. J. Benesty, H. Rey, L. Rey Vega, and S. Tressens, "A nonparametric VSS NLMS algorithm," IEEE Signal Process. Lett., vol. 13, pp. 581-584, Oct. 2006. 18. S.C. Douglas, "Adaptive Filters Employing Partial Updates," IEEE Trans.Circuits SYS.II, vol. 44, pp. 209-216, Mar 1997. 19. Jun Xu; Wei-ping Zhou; Yong Guo; "A Simplified RLS Algorithm and Its Application in Acoustic Echo Cancellation," 2nd International conference on Information Engineering and Computer Science, pp. 1 4, Dec.2010. 20. Mohammed, J.R.; Singh, G.; "An Efficient RLS Algorithm For Output-Error Adaptive IIR Filtering And Its Application To Acoustic Echo Cancellation," IEEE Symposium on Computational Intelligence in Image and Signal Processing, 2007. CIISP 2007, pp. 139-145, April 2007. 21. J. Shynk, "Frequency-domain and multirate adaptive filtering," IEEE Signal Processing Mag., vol. 9, pp. 15- 37, Jan. 1992. 22. D. L. Duttweiler, "A twelve-channel digital echo canceller," IEEE Trans. Commun., vol. 26, no. 5, pp. 647-653, May 1978. 	18-24
----	---	-------

6.	<table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 20%;">Authors:</td> <td>Omar Turath Tawfeeq</td> </tr> <tr> <td>Paper Title:</td> <td>THD Reduction of A Current Source Rectifier-DC Motor Drive Using Single Tuned Filters</td> </tr> </table> <p>Abstract: A current source rectifier (CSR) is commonly used to supply a DC Motor with variable voltage for variable speed applications. A study of THD Reduction using single tuned filters employed to obtain the required low harmonic distortion and nearly unity power factor in A.C supply current over a wide range of operating shaft speed. The external performances of a three-phase CSR-fed separately excited DC motor drive such as power factor, harmonics factor, and efficiency using sinusoidal pulse width modulation (SPWM) control technique are obtained for different speeds and modulation indexes. Separately excited DC motors with armature voltage control provides constant torque operation. The effectiveness of the proposed system (2.3-kW 13A DC motor drive) was verified through computer Matlab simulations.</p> <p>Keywords: Current source rectifier, DC Drive, THD Reduction, Single tuned filters, SPWM.</p> <p>References:</p> <ol style="list-style-type: none"> 1. H.F. Bilgin^{o+}, K.N. Köse^o, G. Zenginobuz^o, M. Ermis^{o+}, E. Nalçacı^{o+}, I. Çadırcı^{o+} and H. Köse* , "A Unity Power Factor Buck Type PWM Rectifier for Medium/High Power DC Motor Drive Applications", "TÜBITAK-METU Information Technologies and Electronics Research Institute TR06531 Ankara-Turkey, (C) 2001 IEEE. 2. SESHAGIRI R. DORADLA, C. NAGAMANI, AND SUBHANKAR SANYAL "A Sinusoidal Pulsewidth Modulated Three-Phase AC to DC Converter-Fed DC Motor Drive " , IEEE Transactions On Industry Applications, Vol. IA-21, No. 6, November/December 1985. 3. Hazım Faruk Bilgin, K. Nadir Köse, Gürkan Zenginobuz, Muammer Ermis, Erbil Nalçacı, Isık Çadırcı, and Hasan Köse "A Unity-Power-Factor Buck-Type PWM Rectifier for Medium / High - Power DC Motor Drive Applications", IEEE Transactions On Industry Applications, Vol. 38, No. 5, September/October 2002 4. Seema P. Diwan, Dr. H. P. Inamdar, and Dr. A. P. Vaidya " Simulation Studies of Shunt Passive Harmonic Filters: Six Pulse Rectifier Load . Power Factor Improvement and Harmonic Control", ACEEE Int. J. on Electrical and Power Engineering, Vol. 02, No. 01, Feb 2011. 5. Rajashekara, K., Bhat, A.K.S., Bose, B.K. "Power Electronics" The Electrical Engineering Handbook Ed. Richard C. Dorf Boca Raton: CRC Press LLC, 2000. 6. Ersoy Kelebekler, Ali Bekir Yildiz, "Analysis of Passive and Active Filters Using Modified Nodal Approach", Compatibility in Power Electronics, CPE '07, Gdansk, Issue Date: May 29-June 1 2007, IEEE. 7. Alexandre B. Nassif, WilsunXu, and WalimirFreitas, "An Investigation on the Selection of Filter Topologies for Passive Filter Applications", IEEE Vol. 24, No. 3, July, 2009. 8. Bashar Abbas Fadeel , "Analysis and Design of Passive Filter to Reduce Line Current Harmonics for Controlled Rectifiers", M. Sc. Thesis, University of Mosul, 2011. (in Arabic). 9. W. SHEPHERD and P.ZAND, "Energy Flow And Power Factor In Nonsinusoidal Circuits", First published, Cambridge University Press, 1979. 10. IEEE Standard 1459-2000, "IEEE Trial-Use Standard Definitions for the Measurement of Electric Power Quantities Under Sinusoidal, Nonsinusoidal, Balanced, or Unbalanced Conditions", 2000, IEEE. 11. Jos Arrillaga and Neville R. Watson, "Power System Harmonics", second Edition, John Wiley & Sons, Ltd, England, 2003. 12. Muhammad H. Rashid, "Power Electronics: Circuits, Devices and Applications", Third Edition, Pearson Prentice Hall, U.S.A, 2004. 	Authors:	Omar Turath Tawfeeq	Paper Title:	THD Reduction of A Current Source Rectifier-DC Motor Drive Using Single Tuned Filters	25-29
Authors:	Omar Turath Tawfeeq					
Paper Title:	THD Reduction of A Current Source Rectifier-DC Motor Drive Using Single Tuned Filters					

	Authors:	Sainath A. Waghmare, Chandan D. Chaudhari, Sumit N. Gavande	
	Paper Title:	Numerical Analysis and Experimental Failure Mode Determination of Composite T -Joint	
7.		<p>Abstract: The use of fibre composite materials in more demanding roles is increasing due to increased performance requirements in various applications. One type of joint in a sandwich panels in superstructure is a T-joint. An existing design consists of panels joined by filler material and overlaminates of the same thickness as the skin laminates. The aim of the research was to determine the methodology to predict the failure mode of the T-Joint under a pull-off tensile loading using Finite Element model. The outcome of the research was that the Finite Element (FE) simulations were used in conjunction to determine the failure mechanism of the T-Joint in the presence of disbonds in the critical location. Stress distributions are investigated by both laboratory tests and numerical modeling, and design criteria for core pieces are presented.</p> <p>Keywords: Sandwich panels, Overlaminates, T-joint, Core pieces, FRP.</p> <p>References:</p> <ol style="list-style-type: none"> 1. Ferry Dharmawan. "The structural integrity and damage tolerance of composite t joint in naval vessel", School of Aerospace, Mechanical and Manufacturing Engineering, RMIT University, Melbourne, Victoria, Australia, November 2008. 2. Toftegaard H, Lystrup A. "Design and test of lightweight sandwich Tjointfor naval ships". Compos A ApplSciManufact 2005;36:1055–65. 3. Toftegaard, H &Lystrup, A, "Design and test of lightweight sandwich Tjoint for naval ships", Composites Part A: AppliedScience and Manufacturing ACMC/SAMPE Conference on Marine Composites (MarComp) 2003, vol. 36, no. 8, pp. 1055-1065. 4. Sheno RA, Hawkins GL. "Influence of material and geometry variations on the behaviour of bonded tee connections in FRP ships." Composites 1992;23:335–45. 5. D.W. Zhou , L.A. Louca and M. Saunders, "Numerical simulation of sandwich T-joints under dynamic loading"Composites: Part B 39 (2008) 973–985 23 December 2007 6. Efsthathios E. Theotokoglou, "FAILURE MODES IN COMPOSITE JOINTS A FINITE ELEMENT STUDY" The National Technical University of AthensGR-157 73 Athens, Greece 7. ST. JOHN, N, GRABOVAC, I, GELLERT, (2000) "Fiber-resin composite research in support of current and future Royal Australian Navy Vessels." International conference Construction Latest Development, London. 8. Yadav S.S. and Chhaphane N. K., "Design and Testing of Lightweight sandwich T-joint of composite material using FEA and experimental technique", ISSN: 2278-0181, IJERT, Vol. 1 Issue 6, August- 2012. 9. KIRSHAN K. CHAWLA, "Composite Materials", Springer publications, Second Edition , reprint (2006). 10. David Cripps, Gurit, http://www.gurit.com 11. M.D. Banea and L.F.M da Silva, "Adhesively bonded joints in composite materials: An overview", DOI: 10.1243/14644207JMDA219, Institution of Mechanical Engineers vol.223, 2009. 12. Christian Berggreen, Christian Lundsgaard-Larsen, Kasper Karlsen, Claus Jenstrup and Brian Hayman, "Improving performance of polymer fiber vessels-Part I: Design aspects", 16th International Conference On Composite Materials, Kyoto Japan, 2007. 13. S.M.R. Khalili and A.Ghaznavi, "Numerical analysis of adhesively bonded T-joints with structural sandwiches and study of design parameters", Elsevier, International Journal of Adhesion & Adhesives 31 (2011) 347–356, February 2011. 14. www.azom.com 15. X.X. Dai and J.Y. Richard Liew, "Fatigue performance of lightweight steel_concrete_steel sandwich systems", Elsevier, Journal of Constructional Steel Research 66 ,256_276, July 2009. 16. http://www.dnv.com 17. Keun-Il Song, Ji-Young Choi, Jin-HweKweon, Jin-Ho Choi and Kwang-Soo Kim, "An experimental study of the insert joint strength of composite sandwich structures", Elsevier, Composite Structures 86 (2008) 107–113, 2008 18. http://www.cetec.demon.co.uk 19. A.P. Mouritz, E. Gellert, P. Burchill and K. Challis, " Review of advance composite structure for Naval ships and submarines ", Elsevier, Composite Structure 53 (2001) 21-41, 2001. 20. E. Bozhevolnaya, A. Lyckegaard and O.T. Thomsen, "Novel design of foam core junctions in sandwich panels", Elsevier, Composites: Part B 39 (2008) 185–190. 	30-34
8.		<p>Authors: Kushal M. L, V. Venkateswarlu</p> <p>Paper Title: Design and Implementation of Phase Modulation Using PLL for Polar Transmitter</p> <p>Abstract: In this paper the phase modulation of polar transmitter has been implemented. The circuit for implementing phase modulation consists of Phase Lock Loop (PLL), Sigma Delta Modulator (SDM) and differentiator. The input signal is applied at the differentiator which will convert phase component to the frequency of the signal. The obtained frequency signal is given to the SDM which will convert the analog signal to the digital signals. The SDM should have four bits of resolution, equivalently 36 dB signal-to-noise-and-distortion-ratio (SNDR) for a 200 kHz bandwidth. For supply voltages from 2.5 V to 3 V, the current supply is desired to be less than 20 mA. The PLL consists of the reference signal of 125MHz, and output voltage around 2-3GHz with the VCO gain of 0.277GHz/V. The circuit of PLL, SDM and differentiator are implemented on the Cadence Virtuoso platform.</p> <p>Keywords: Polar transmitter, phase modulation, Phase lock loop, Sigma delta modulator, differentiator.</p> <p>References:</p> <ol style="list-style-type: none"> 1. John Groe., "Polar Transmitters for Wireless Communications," IEEE Communications, pp. 58-63, Sept. 2007. 2. G. Brenna et al., "A 2-GHz Carrier Leakage Calibrated Direct-Conversion WCDMA, 2002. 3. Transmitter in 0.13-mm CMOS," IEEE J. Solid-State Circuits, pp. 1253–62, Aug. 2004, 4. T. Sowlati et al., "Quad-band GSM/GPRS/EDGE Polar Loop Transmitter," IEEE J. Solid State Circuits, pp. 2179–89, Dec. 2004. 5. S Cripps, "Advanced Techniques in RF Power Amplifier Design," Norwell, MA: Artech House, 2002 6. Michael Youssef, Alireza Zolfaghari, Behnam Mohammadi, Hooman Darabi and Asad A., "A Low-Power GSM/EDGE/WCDMS Polar Transmitter in 65-nm CMOS," IEEE J. Solid State Circuits, pp. 3061-3074, Dec. 2011. 7. T. A. D. Riley, M.A. Copeland, and T. A. Kwasniewski, "Delta-Sigma modulation in fractional frequency synthesis," IEEE J. Solid State Circuits, vol. 28, no. 5, pp 553-559, May 1993. 8. J.Groe, "Highly Linear Phase Modulation," US patent 10/420,952. 9. B.Razavi, "Monolithic Phase-Locked Loops and Clock-Recovery Circuits", IEEE Press, 1996. 10. J. Maneatis, "Self-Biased, High-Bandwidth, Low-Jitter 1-to-4096 Multiplier Clock Generator PLL", IEEE J. Solid-State Circuits, vol. 38, no.11, pp. 1795-1803. Nov. 2003. 	35-41

	<ol style="list-style-type: none"> 11. F. Gardner, "Charge-pump phase-lock loops," IEEE Trans. Communications., vol COM-28, no. 11, pp 1849-1858, Nov. 1980. 12. "Gate-diffusion input (GDI)—A novel power efficient method for digital circuits: A design methodology," presented at the 14th Int. ASIC/SOC Conf., Washington, DC, Sept. 2001. 13. Arkadiy Morgenshtein, Alexander Fish, and Israel A. Wagner, "Gate-Diffusion Input (GDI): A Power-Efficient Method for Digital Combinatorial Circuits," IEEE Transactions on Very Large Scale Integration (VLSI) systems, vol. 10, no. 5, pp. 566-581 October 2002. 14. Chang, B., Park, J., & Kim, W. "A 1.2 GHz CMOS dual modulus prescaler using new dynamic D-type flip-flop", IEEE Journal of Solid-State Circuits, vol 31(5), pp. 749–752, 1996 15. Larsson, P., "High-speed architecture for a programmable frequency divider and a dual-modulus prescaler", IEEE Journal of Solid-State Circuits, vol. 31(5), pp.744–748, 1996 16. Yang, C.-Y., Dehng, G.-K., Hsu, J.-M., & Liu, S.-I, "New dynamic flip-flops for high-speed dual-modulus prescaler", IEEE Journal of Solid-State Circuits, vol. 33(10), pp. 1568–1571, 1998. 17. Chi, B., & Shi, B., "New implementation of phases witching technique and its applications to GHz dual-modulus prescaler", IEE Proceedings of the Circuits Devices and Systems, vol. 150(5), pp. 429–433, 2003 18. Yang, D.-J., & Kenneth, K. O., "A 14-GHz 256/257 dual modulus prescaler with secondary feedback and its application to a monolithic CMOS 10.4-GHz phase-locked loop", IEEE Transactions on Microwave Theory and Techniques, vol. 52(2), pp. 461–468, 2004. 19. Perrott, M. H., Tewksbury III, T. L., & Sodini, C. G. "A 27-mW CMOS fractional-N synthesizer using digital compensation for a 2.5-Mb/s GFSK modulation", IEEE Journal of Solid State Circuits, vol. 32(12), pp. 2048–2060, 1997 20. Zarei, H., Shoei, O., Fakhr, S. M., & Zakeri, M. M., "A 1.4 GHz/2.7 V programmable frequency divider for DRRS standard in 0.6lm CMOS process", IEEE International Conference on Electronics, Circuits and Systems, 2000 21. Rogenmoser, R. & Huang, Q., "An 800-MHz 1-lm CMOS pipelined 8-b adder using true single-phase clock logic-flip-flops", IEEE Journal of Solid-State Circuits, vol. 31(3), pp. 401–409, 1996. 22. Gu R. X., Sharaf, K. M., & Elmasry, M. I., "High-performance digital VLSI circuit design", Dordrecht: Kluwer Academic Publishers, 1996. 23. Issachsu, and Howard C. Luong. "A 70-MHz Continuous-time CMOS Band-pass $\Sigma\Delta$ Modulator for GSM Receivers" ISCAS 2000 - IEEE International Symposium on Circuits and Systems, Geneva, Switzerland, volume 3, page(s): 750-753, May 28-31, 2000. 24. Jesus Arias, Peter Kiss, Vladimir Prodanov, Vito Bocuzzi, Mihai Banu, David Bisbal, Jacinto San Pablo, Luis Quintanilla, and Juan Barbolla, "32-mW 320-MHz Continuous-Time Complex Delta-Sigma ADC for Multi-Mode Wireless-LAN Receivers," IEEE Journal of solid-state circuits, Volume 41, no. 2, page(s): 339-351, February 2006. 25. Kuskie C., Zhang B., Schreier R., "A decimation filter architecture for GHz delta-sigma modulators," IEEE International Symposium on Circuits and Systems, 1995. Volume 2, pages(s):953 – 956, 28 April-3 May 1995. 26. C.S. Kim, Y.H. Kim, S.B. Park, "New CMOS Linear Transconductor," Electronics Letters, Vol.28 No.21, October 1992. 27. J.E.Kardontchik, "Introduction to the design of Transconductor-Capacitor Filters", KWP, 1992. 28. Debasis Parida , "A Novel High Speed CMOS Comparator With Low Power Disipation And Low Offset", Department Of Electronics & Communication Engineering National Institute Of Technology Rourkela ,2010. 29. Sokolovic M., Jovanovic B., Damnjanovic M., "Decimation filter design," 24th International Conference on Microelectronics, Volume 2, pp.601 – 604, 16-19 May 2004. 30. Saramaki T., "A systematic technique for designing highly selective multiplier-free FIR filters," IEEE International Symposium on Circuits and Systems, Volume 1, Page(s):484 - 487 ,11-14 June 1991. 31. Yonghong Gao, Lihong Jia, Tenhunen, H., "A fifth-order comb decimation filter for multi-standard transceiver applications," IEEE International Symposium on Circuits and Systems. ISCAS 2000 Geneva. Volume 3, Page(s):89 - 92 , 28-31 May 2000 . 					
	<table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 15%;">Authors:</td> <td>G. Anuradha</td> </tr> <tr> <td>Paper Title:</td> <td>Self Automated Tool in Vehicular System That Identifies the Air Pollution and the Future of E-Governs</td> </tr> </table>	Authors:	G. Anuradha	Paper Title:	Self Automated Tool in Vehicular System That Identifies the Air Pollution and the Future of E-Governs	
Authors:	G. Anuradha					
Paper Title:	Self Automated Tool in Vehicular System That Identifies the Air Pollution and the Future of E-Governs					
9.	<p>Abstract: Air pollution is a major issue that we face today. Pollution zone in during earlier century was low but keeps growing towards the current. Studies revealed the fact the concentration in organic and inorganic toxic elements keeps growing depending upon the region and vehicle type. There is a growing demand for the environmental pollution monitoring and control systems. Gas sensors help to detect gas molecules and the concentration of the gas analytical methods of identifying the gases has greater disadvantage than with fairly accurate and selective gas reading.</p> <p>Keywords: Embedded control, Monitoring system, Remote monitoring, sensor.</p> <p>References:</p> <ol style="list-style-type: none"> 1. "The use of electrochemical sensors for monitoring urban air quality in low-cost, high-density networks", M. I. Mead^{1*}, O.A.M. Popoola¹, G. B. Stewart¹, P. Landshoff³, M. Calleja², M. Hayes², J. J. Baldovi¹, T.F.Hodgson¹, M. W. McLeod¹, J. Dicks⁴, A. Lewis⁴, J. Cohen⁵, R. Baron⁶, J. R. Saffell⁶, and R. L. Jones^{1*}. 2. "A study on air pollution by automobiles in Bangalore city", Mahadevappa harish, iisc, management research and practice Vol.4 Issue 3(2012) PP: 25-36. 3. "A mobile GPRS-sensors Array for Air pollution monitoring", Imran Zualkernan, Fadi Aloul, IEEE sensors Journal, Vol.10, No.10, October 2010. 4. "Pollution Monitoring system using wireless sensor network in Visakhapatnam", P.Vijnatha Raju, RVRS.Aravind, B.Sangeeth Kumar, IJETT, Volume4 Issue4-April 2013. 5. "Smart sensor networks: Technologies and application for green growth", OECD, 2009. 6. "Automated control system for air pollution detection in vehicles", chandrasekaran, sudharshan muthukumar, sabeshkumar rajendran, 2013 4th International conference on intelligent systems, modeling and simulation. 7. "Implementation of web server using ARM for intelligent monitoring", pratik padia, R.Manohari, Sandeep Kethi redid, ASAR International Conference, ISBN:978-81-927147-0-7. 	42-44				