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S. No	Volume-2 Issue-6, April 2014, ISSN: 2319-6378 (Online) Published By: Blue Eyes Intelligence Engineering & Sciences Publication Pvt. Ltd.		Page No.
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	Paper Title:	Embedding Encrypted Data in Video Using Symmetric Key Cryptography	
	<p>Abstract: Video data hiding is still an important research topic due to the design complexities involved. We propose a new video encrypted data hiding method that makes use of erasure correction capability of repeat accumulate codes and superiority of forbidden zone data hiding as well as DNA cryptography logic is used for encryption and decryption of the data. This paper also proposes a unique cipher text generation procedure as well as a new key generation procedure. DNA cryptography is one of the major concerned areas of computer and data security and a very promising direction in cryptography research. Selective embedding is utilized in the proposed method to determine host signal samples suitable for data hiding. This method also contains a temporal synchronization scheme in order to withstand frame drop and insert attacks. Finally, to demonstrate the performance of the proposed method, its implementation is explained and the results are analyzed.</p> <p>Keywords: Cipher text, Data hiding, decryption, encryption, forbidden zone data hiding, key generation, repeat accumulate codes, selective embedding, security.</p> <p>References:</p> <ol style="list-style-type: none">1. B. Roy, G. Rakshit, P. Singha, A. Majumder and D. Datta, "An improved Symmetric key cryptography with DNA based strong cipher", Department of Computer Science and Engineering Tripura Institute of Technology, Narsingarh, Tripura, India.2. Ersin Esen and A. Aydin Alatan, "Robust Video Data Hiding Using Forbidden Zone Data Hiding and Selective Embedding" in IEEE transactions on circuits and systems for video technology, vol. 21, no. 8, august 2011.3. Garfinkel Simson, Web Security, Privacy & Commerce, 2nd Edition, O'Reilly Publisher, November 2001.4. M. Wu, H. Yu, and B. Liu, "Data hiding in image and video: I. Fundamental issues and solutions," IEEE Trans. Image Process., vol. 12, no. 6, pp. 685-695, Jun. 2003.5. M. Wu, H. Yu, and B. Liu, "Data hiding in image and video: II. Designs and applications," IEEE Trans. Image Process., vol. 12, no. 6, pp. 696-705, Jun. 2003.6. M.M.Mansour,"A turbo-decoding message-passing algorithm for sparse parity-check matrix codes,"IEEE Trans. Signal Process.,vol. 54, no. 11,pp. 4376-4392, Nov. 2006.7. A. Sarkar, U. Madhow, S. Chandrasekaran, and B. S. Manjunath, "Adaptive MPEG-2 video data hiding scheme," in Proc. 9th SPIE Security Steganography Watermarking Multimedia Contents, 2007, pp. 373-376.8. K. Solanki, N. Jacobsen, U. Madhow, B. S. Manjunath, and S. Chandrasekaran, "Robust image-adaptive data hiding using erasure and error correction," IEEE Trans. Image Process., vol. 13, no. 12, pp. 1627-1639, Dec. 2004.9. E. Esen and A. A. Alatan, "Forbidden zone data hiding," in Proc. IEEE Int. Conf. Image Process., Oct. 2006, pp. 1393-1396.		
2.	Authors:	Mohammed Hussein Baqir	
	Paper Title:	Development Controller using PC for Generation PWM	
	<p>Abstract: Design and construction of microcomputer for inverter controlling, this inverter is designed by using power electronics switches type MOSFET. The MOSFET's are controlled by using pulses depending on sampled sinusoidal Pulse Width Modulation (SPWM) technique with frequency ratio changing. Constant voltage to frequency (V/F) ratio is maintained through the program leading to constant flux speed range. The range of inverter output frequency is (0→50 Hz). This inverter output control the 3-ph induction motor speed.</p> <p>Keywords: MOSFET, (V/F), (0-50Hz).</p> <p>References:</p> <ol style="list-style-type: none">1. A.P. Malvino, "Digital computer electronics an introduction to microcomputers" second edition Mc. Graw- Hill Inc. 2008.2. M. A. AL- Taei, "Microprocessor based 3- phase PWM waveform generator" proc. Of 11th Iraq scientific engineering conf. on electrical engineering Baghdad, 1993.3. You Lee and Y. Yith Sum, "Adaptive harmonic control in PWM inverters with fluctuating input voltage", IEEE. Trans. Ind. Electron, Vol. IE-33, NO. 1, FEB. 2009.4. S. Muruge San, "An overview of electric motor for space application", IEEE. Trans. Ind. Electron. Vol. IECI-28, NO. 4, Nov. 2007.5. Bowes, S. R., and Midoun. A, "Microprocessor Implementation of new optimal PWM switching strategies", IEEE. Proc, Vol. 135, Pt.B, NO. 5, Sep. 1988.6. M. Vaarnovisky, "A microcomputer based control signal generator for a three phase switching power inverter", IEEE transaction on industry application Vol. 19, NO. 2, march 1999.7. Intel Corporation, "Microcontroller handbook", 2010.8. Mohammed. H. B, "Variable dc input voltage source inverter based on microcontroller" MSc. Thesis, university of technology Baghdad, 2005.		
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	Paper Title:	Evaluating The Financial Implication of Power Harmonics on Electricity Corporation of Ghana (ECG)'s Distribution Networks and Customers	

	<p>Abstract: The survival and sustainability of businesses, especially in volatile developing country markets, require that businesses frugally manage input cost such as the cost of electric power. However, Power Harmonics (PH) that cause excessive dissipation of energy as heat can raise the cost of electric power for companies and for the power suppliers as well. Unknown to them, many businesses in Ghana may have been exposed to serious financial losses as a result of the presence of power harmonics in the distribution network. Such PH is essentially a negative externality caused by the users of PH producing gadgets and visited on other unsuspecting users of electric power who are essentially bystanders: a classic case of a negative externality. PH is a major contributor to the poor state of power quality internationally and have been in existence since the first alternating current generator went online more than 100 years ago. This paper presents an analysis of the financial implication of power harmonics in the distribution network of the Electricity Company of Ghana (ECG) and on customers of the company using a mixed methodology of qualitative and quantitative approaches. The results from the power system monitoring carried out confirm the presence and deleterious effects of power harmonics in the distribution network. Unfortunately, most customers are not aware of the negative effects of power harmonics. Differentiating the effect of PH from other poor power quality issues has cost consequences for most customers. Analysed results from the power system monitoring carried out shows that more than Gh¢2,396,814.00 (\$1,261, 481.00 at an exchange rate (\$/Ghc) of 1.9 in 2012) per annum is wasted as heat in the distribution network. There is therefore the need to introduce punitive measures against the generation of harmonics into the distribution system in order to incentivise the promulgators of PH to internalize the externality. This will ensure the survival and sustainability of businesses in Ghana’s volatile and under-resourced industrial sector.</p> <p>Keywords: (ECG), PH.</p> <p>References:</p> <ol style="list-style-type: none">1. Hamilton Michael, (2005), Presentation on “Power System Harmonics”, http://www.geig.net/dlfiles/MikeHam_HarmonicsPress_GEIG_13072005.pdf. Cited on 15th November, 20122. Rice, D.E. (1986)“Adjustable speed drive and power rectifier harmonic—their effect on power systems components,” IEEE Trans. on Ind. Appl., Vol. IA-22, No. 1, pp. 161—177.3. IEEE Task Force, (1985),“The Effects of Power System Harmonics on Power System Equipment and Loads”, IEEE Trans. Power Apparatus and Systems, Vol. PAS-104, pp 2555-2563.4. Kasma, N; Drossos, N. Stavropoulos, D. and Papathanassiou, S. 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4.	<table><tr><td>Authors:</td><td>Rahul Sood, Ashwani Kumar, Gurpreet Singh Batth</td></tr><tr><td>Paper Title:</td><td>Experimental Utilization of 2-Ethoxy Ethyl Acetate as a Blend in a Single Cylinder CI Engine</td></tr></table> <p>Abstract: In this investigating study diesel fuel was used as a reference fuel for 2-Ethoxy Ethyl Acetate –diesel blends. The blends containing 5, 10 and 15% of 2-Ethoxy Ethyl Acetate fuel by volume are tested on test rig developed for the experimentation. All the tests were conducted in steady state and were set at constant engine speed 1500 RPM. With the addition of oxygen in the fuel, it has been observed that the emission contents reduce remarkably. Moreover there is an increase in the Brake Horse Power and Brake Thermal Efficiency of the engine. So it can be concluded that the addition of 2-Ethoxy Ethyl Acetate by 10% in diesel not only helps to reduce the exhaust emission but also increases the performance of the diesel engine. 10% blend increases the BHP by 7.6% and BTE increases by 7.2 % at full load conditions. Also this blend ration decreases the CO % by 16%, HC by 11.9% and Smoke Opacity reduces by 19.11%. The experimental results prove that the use of 2-Ethoxy Ethyl Acetate fuel as a blend improves the engine operation and reduces the environmental pollution.</p> <p>Keywords: 2-Ethoxy Ethyl Acetate, Oxygenated fuel, Diesel engine, Engine emission, Exhaust smoke, Carbon monoxide, Carbon dioxide, Hydrocarbon.</p> <p>References:</p> <ol style="list-style-type: none">1. Kent Nord and Dan Haupt, “Particulate Emissions from an Ethanol Fueled Heavy Duty Diesel Engine Equipped with EGR, Catalyst and DPF” 4530r11: SAE Paper 2004	Authors:	Rahul Sood, Ashwani Kumar, Gurpreet Singh Batth	Paper Title:	Experimental Utilization of 2-Ethoxy Ethyl Acetate as a Blend in a Single Cylinder CI Engine	21-23
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5.	Authors:	Jagpreet Singh, Ashwani Kumar, Satbir Singh Sehgal
	Paper Title:	Experimental Studies on Heat Transfer Augmentation of a Heat Exchanger with Swirl Generators Inserts
	<p>Abstract: Convective heat transfer characteristics within a heat exchanger with twisted tapes of different cuts and materials have been investigated experimentally. Effect of twisted tape of different cuts (square, circular and triangular) inside the inner tube of single unit on heat transfer and friction factor for heating of water for Reynolds number range 500-3000 was studied experimentally. The results obtained from the twisted inserts of GI, Al and Cu materials are compared and the experimental results reveal that the among the three different materials of inserts, Copper inserts performs better and too with square cuts.</p> <p>Keywords: Heat Exchanger, Swirl Generators, Inserts, Nusselt Number, Reynolds Number</p> <p>References:</p> <ol style="list-style-type: none"> 1. Smith Eiamsa-ard, Somsak Pethkool, Chinaruk Thianpong and Pongjet Promvonge, "Turbulent flow heat transfer and pressure loss in a double pipe heat exchanger with louvered strip inserts", International Communications in heat and Mass Transfer, Volume 35, Pages 120-129, Issue 2, February 2008. 2. Smith Eiamsa-ard , Chinaruk Thianpong , Petpices Eiamsa-ard and Pongjet Promvonge, "Convective heat transfer in a circular tube with short-length twisted tape insert", International Communications in Heat and Mass Transfer, Volume 36, Pages 365-371, Issue 4, April 2009. 3. S. Eiamsa-ard and P. Promvonge, "Enhancement of Heat Transfer in a Circular Wavy-surfaced Tube with a Helical-tape Insert", International Energy Journal, Pages 29-36, August 2007. 4. Suhas V. Patil and P. V. Vijay Babu, "Performance Comparison of Twisted Tape and Screw Tape Inserts in Square Duct", International Conference on Advanced Science, Engineering and Information Technology, Pages 50-55, January 2011. 5. Smith Eiamsa-ard and Pongjet Promvonge, "Heat Transfer and Pressure Drop Characteristics in a Double-Pipe Heat Exchanger Fitted with a Turbulator", International Energy Journal, Pages 1-5, January 2006. 6. B Salam and M M K Bhuiya, "An Experimental Study of Tube-Side Heat Transfer", International Conference on Mechanical Engineering, Pages 1-4, December 2007. 7. Smith Eiamsa-ard and Pongjet Promvonge, "Heat transfer characteristics in a tube fitted with helical screw-tape with/without core-rod inserts", International Communications in Heat and Mass, Volume 34, Issue 2, Pages 176-185, February 2007. 8. Paisarn Naphon, Effect of coil-wire insert on heat transfer enhancement and pressure drop of the horizontal concentric tubes, International Communications in Heat and Mass Transfer 33 (2006) 753-763. 	24-27
	Authors:	Rubi Choubey, Md. Arif
	Paper Title:	Area Optimized and Low Power using Modified Booth Multiplier for Unsigned Numbers
6.	<p>Abstract: Power consumption and small area is very important for fabricating DSP system and high performance system, requirement of present scenario computer system is dedicated for very high speed and low power unique multiplier unit for signed and unsigned number therefore in this paper focus on unsigned number by using modified booth multiplier. The unsigned 4 bit and 8 bit implementation done by some modification in booth multiplier modified booth multiplier come out to make efficient multiplier reduce N/2 partial product. The parallel multiplier 4 bit and 8 bit modified booth multiplier does the computation using lesser adder and lesser iterative step. The implementation of unsigned 4 bit and 8 bit done in Xilinx ISE Design suite 12.1 tool by using VHDL, model Sim.</p> <p>Keywords: Array Multiplier Booth multiplier, Modified Booth Multiplier, Model Sim, Partial Product, Unsigned, VHDL, Xilinx.</p> <p>References:</p> <ol style="list-style-type: none"> 1. Ravindra P.Rajput, M.N Shanmukha Swamy " High Speed Modified Booth Encoder Multiplier for Signed and Unsigned numbers" 14th International Conference on Modelling and Simulation 978-0-7695-4682-7/12© 2012 IEEE. 2. W. C. Yen, C. W. Jen, "High Speed Booth encoded Parallel Multiplier Design," IEEE transactions on Computer, Vol. 49, No. 7, pp. 692-701, July 2000. 3. A.D. Booth, "A Signed Binary Multiplication Technique Quarterly Journal of Mechanics and Applied mathematics, Vol-IV, pt-2-1951. 4. Rainishmi Ranjan, Pramod Mohanty, "A New VLSI Architecture of Parallel Multiplier based on Radix- 4Modified Booth Algorithm Using VHDL", Inter-national Journal of Computer Science & EngineeringTechnology, ISSN:2229-3345, Vol. 3 No. 4 April 2012. 5. Lenardo Lde Oliveira, Eduaro Costa, Sergio Bampi,Joao Baptista and Jose Monteiro, "array hybridMultiplier versus Modified Booth Multiplier: comparing Area and Power consumption of layout Implementations of signed Radix-4 Architecture", IEEE, 2004. 6. A.S.Prabhu, V.Elakya, "Design of modified Low Power Booth Multiplier",IEEE, 2012. 7. Kavita, Jasbir Kaur, "Design and Implementation of an Efficient Modified Booth Multiplier using VHDL", International Conference on Emerging Trend in Engineering and Management, ISSN:2231-0347, Vol.3(3, July2013). 8. Sukhmeet Kaur, Suman, Manpreet Singh Manna, "Implementation of Modified Booth Algorithm (Radix-4) and its comparison with Booth Algorithm(Radix-2)", Advance Electronic and Electric Engineering, ISSN 2231-1297, Vol.3, November 6(2013), pp. 683-690. 9. N.H.E. Weste, K.Eshraghain, "Principle Of CMOS VLSI Design, A systems Perspective", Pearson Education, 2010. 	28-32
	Authors:	Vedkiran Saini, Parvinder Bangar, Harjeet Singh Chauhan
	Paper Title:	Study and Literature Survey of Advanced Encryption Algorithm for Wireless Application
7.		

	<p>Abstract: Today increases any wireless communication security is crucial during data transmission. The encryption and decryption of data is the main challenge faced in the wireless communication for security of the data transmission source to destination. In this paper we present the literature study of cryptography security AES algorithm and its present application in communication, data communication and wireless communication. In this paper, we use the Advanced Encryption Standard (AES) which works on a 128 bit data encrypting it with 128 bits of keys for ensuring security. In this paper literature study of AES algorithm and selection AES algorithms for wireless communication application and design verilog AES sub block add round key, mix column, s-box using Xilinx ISE 9.1i software for a Spartan3 FPGA device</p> <p>Keywords: Advanced Encryption Standard (AES), Rinjdael, Cryptography,</p> <p>References:</p> <ol style="list-style-type: none">1. Xinmiao Zhang and Keshab K. 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Yoshimura, M. et al, "Defect and Fault Tolerance in VLSI and Nanotechnology Systems (DFT)", IEEE International Symposium on Page(s):278 – 283, 201313. Hui QIN, Tsutomu SASAO, Yukihiro IGUCHI "An FPGA Design of AES Encryption Circuit with 128-bit Keys" GLSVLSI'05, ACM 200514. Chih-Peng Fanand and Jun-Kui Hwang "FPGA Implementations of High Throughput Sequential and Fully Pipelined AES Algorithm" International journal of Electrical Engineering, vol.15, no.6, pp. 447-455, 2008.15. Mehran Mozaffari-Kermani and Arash Reyhani-Masoleh "Efficient and High Performance Parallel Hardware Architecture for the AES-GCM" IEEE Transactions On Computers, vol.61, no. 8, August 2012.16. Archna Garg et al, "Efficient Field Programmable Gate ArrayImplementation of Advanced Encryption Standard Algorithm using VHDL", International Journal of Engineering Trends and Technology (IJETT) – Volume 4 Issue 9, pp. 3956-3961,September 201317. Saambhavi Baskaran and Pachamuthu Rajalakshmi "Hardware Software Co-Design of AES on FPGA" ICACCI '12,ACM August 2012.18. Ashwini M. Deshpande, Mangesh S. Deshpande and Devendra N. Kayatanavar "FPGA Implementation of AES Encryption and Decryption" International Conference on Control, Automation, Communication and Energy conservation -2009.19. Richa Sharma, Purnima Gehlot, S. R. Biradar, "VHDL Implementation of AES-128, UACEE International Journal of Advances in Electronics Engineering – IJAE, Volume 3 : Issue 2, [ISSN 2278 – 215X],pp-17-20, 201320. X. Zhang and K. K. Parhi, "High-speed VLSI architectures for the AES algorithm,"IEEE Transactions on Very Large Scale Integration Systems, vol.12, issue 9, pp.95 967, Sep. 2004.21. Jin Gong ,Wenyi Liu, Huixin Zhang "Multiple Lookup Table- Based AES Encryption Algorithm Implementation" Elseveir- 2012 vol.25 pg no.842 – 847.22. Biham, Eli and Adi Shamir, Differential Cryptanalysis of the Data Encryption Standard, Springer Verlag, 1993.23. National Institute of Standards and Technology, "Federal Information Processing Standards Publication 197", 200124. jin Gong ,Wenyi Liu, Huixin Zhang "Multiple Lookup Table- Based AES Encryption Algorithm Implementation" Elseveir- vol.25 pg no.842 – 847, 2012.	33-37				
8.	<table><tr><td>Authors:</td><td>M. Krupa Swaroopa Rani, G. Kiran Kumar, M. Krishnaiah, K.Kameswara Rao</td></tr><tr><td>Paper Title:</td><td>Clutter Removal for RADAR Wind Profiler using Wavelet Thresholding</td></tr></table> <p>Abstract: Atmospheric Signal processing has been one field of signal processing where there is a lot of scope for development of new and efficient tools for cleaning of the spectrum, detection and estimation of the desired parameters. The field of digital signal processing is a very active area for research and applications. Atmospheric signal processing deals with the processing of the signals received from the atmosphere when manually stimulated using atmospheric Radar. Removal of clutter in the radar wind profiler is the utmost important consideration in radar. In this paper, we implement wavelet thresholding for removing clutter from wind profiler Radar data. By applying the concept of discrete multi-resolution analysis and non-parametric estimation theory, we develop wavelet domain thresholding rules, which identify the coefficients relevant for clutter and suppress them and increases the accuracy of wind vector reconstruction.</p> <p>Keywords: Clutter, Signal Processing, Wind Profiler, Wavelet Thresholding.</p> <p>References:</p> <ol style="list-style-type: none">1. Keeler, R. J. and Passarelli, R. E., Signal processing for atmospheric radars, in Radar in Meteorology, edited by D. Atlas, chap. 20a, 199–229, American Meteorological Society, Boston, 1990.2. Carter, D., Gage, K. S., Ecklund, W. L., Angevine, W. M., Johnston P. E., Riddle, A. C., Wilson, J., and Williams, C. R., Developments in UHF lower tropospheric wind profiling at NOAA's Aeronomy Laboratory, Radio Sci., 30, 977–1001, 1995.3. Wilfong, T. L., Merritt, D. A., Weber, B. L., and Wuertz, D. B., Multiple signal detection and moment estimation in radar wind profiler spectral data, submitted to J. Atmos. Oceanic Technol.,1999b.4. Schmidt, G., R'uster, R., and Czechowsky, P., Complementary code and digital filtering for detection of weak VHF radar signals from the Mesosphere, IEEE Trans. Geosci. Electron., GE-17, 154–161, 1979.5. Sulzer, M. and Woodman, R., Quasi-complementary codes: A new technique for MST radar sounding, Radio Sci., 19, 337–344, 1984.	Authors:	M. Krupa Swaroopa Rani, G. Kiran Kumar, M. Krishnaiah, K.Kameswara Rao	Paper Title:	Clutter Removal for RADAR Wind Profiler using Wavelet Thresholding	38-42
Authors:	M. Krupa Swaroopa Rani, G. Kiran Kumar, M. Krishnaiah, K.Kameswara Rao					
Paper Title:	Clutter Removal for RADAR Wind Profiler using Wavelet Thresholding					

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	Authors: Sarseena C.K, Yadhu R.B	
	Paper Title: Fractional Fourier Domain MRI Reconstruction Using Compressive Sensing Under Different Random Sampling Scheme	
9.	<p>Abstract: In clinical Magnetic Resonance Imaging (MRI), any reduction in scan time offers an improvement in patient comfort problem. Compressive sensing introduces a new technique to image reconstruction from less amount of data. It will reduce imaging time in MRI. Compressive sensing exploit the sparsity of the signal. In this paper fractional Fourier is used as sparsifying transform and signal sampled using different random sampling method. Such as gaussian, bernoullie, and poisson distribution. Then MRI accurately reconstructed from very highly under sampled data using Maximum likelihood estimation.</p> <p>Keywords: Compressive sensing, Fractional Fourier transform, maximum likelihood estimation</p> <p>References:</p> <ol style="list-style-type: none"> 1. Shanon sampling theorem . Its various extension and application. A tutorial review. Proceedings of IEEE 1977 2. The Origins of the Sampling Theorem: Han Dieter Luke. Aachen university of technology. 3. Marsellie GJ, de Beer R, Mehlkopf AF, Van ormondit d. on uniform phase encode distribution for MRI scan time reduction. J Magn Reson 1996;111:70-75. 4. Super resolution MRI images using compressive sensing ICEE2012, Samad roohi Compt engg&IT dept. Amirkabir university of Technology, jafar zamani, M noorhosseini. 5. M. Lustig, D. Donoho, and I. M. Pauly, "Sparse MRI: The application of compressed sensing for rapid MR imaging," Magnetic Resonance in Medicine, vol. 58, no. 6, pp. 1182-1195, 2007. 6. Sampling of Sparse Signals in Fractional Fourier Domain Ayush Bhandari (1) and Pina Marziliano (Author manuscript, published in "SAMPPTA'09, Marseille : France (2009)" 7. Comparison of Reconstruction Algorithms for Images from Sparse-Aperture Systems. J.R. Fienup, D. Griffith, L. Harrington, Institute of Optics, Wilmot 410, University of Rochester, Rochester, Published in Proc. SPIE 4792-01, Image Reconstruction from Incomplete Data II, Seattle, WA, July 2002 8. D.L Donoho, "Compressed sensing," IEEE Trans. Information Theory, vol. 52, no. 4, pp. 1289-1306, September 2011 9. R.G. Baranuik, "Compressive sensing," IEEE Signal Processing Magazine, vol. 24, no. 4, pp 5406-5425, Dec 2008 10. E.J. Candes and M.B. Walkin, "An introduction to compressive sampling," IEEE Signal Processing Mag., vol. 25, no. 2, pp. 21-30, March 2008 11. Sampling of Sparse Signals in Fractional Fourier Domain Ayush Bhandari (1) and Pina Marziliano (Author manuscript, published in "SAMPPTA'09, Marseille : France (2009)" 12. Computation of the Fractional Fourier Transform Adhemar Bultheel and Hector E. Martinez Sulbaran Dept. of Computer Science, Celestijnenlaan 200A, B-3001 Leuven, 13. Application of the Fractional Fourier Transform to Image Reconstruction in MRI Vicente Parot, Carlos Sing-Long, Carlos Lizama, Cristian Tejos Member, IEEE, Sergio Uribe, and Pablo 14. Comparison of Reconstruction Algorithms for Images from Sparse Aperture Systems J.R. Fienup, D. Griffith, L. Harrington, A.M, Published in Proc. SPIE 4792-01, 15. Fundamentals of statistical signal processing Steven M Key 	43-45