Dec/26/2012

errata_s5u1c17001h2_1

S1C17 Manual errata

ITEM:			
Object manuals	Document codes	Items	Pages
S5U1C17001H2 User Manual (ICDmini Ver2.0)	411153403	**4.1.1 Target Interface Connector**	14
Same as above	Same as above	**8.3 Notes on Use of S5U1C17001H**	32,33

(Error)

4.1.1 Target Interface Connector

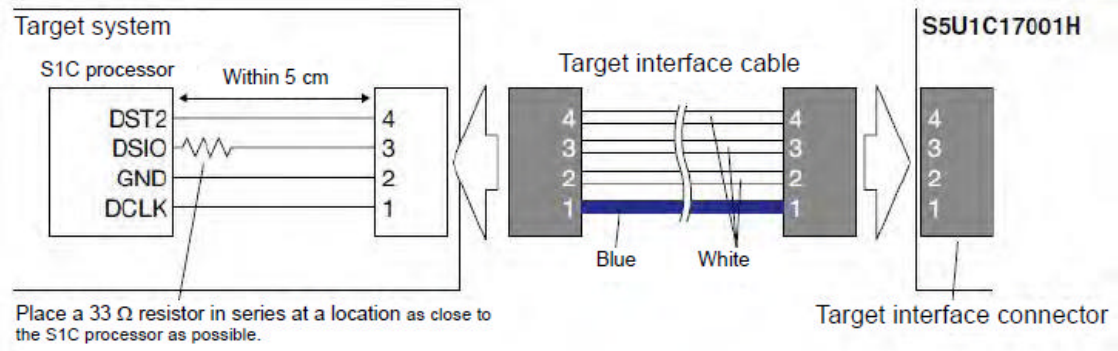

Figure 4.1.1.1 Connecting the Target System

Notes:

- When wiring the S1C processor to the target connector for connecting the S5U1C17001H, insert a 33ohm resistor in series between the S1C processor DSIO pin and the connector. This resistor must be placed as close to the S1C processor as possible. Although the system can operate without this 33ohm resistor, we recommend inserting this resistor to prevent malfunctions. The other pins are connected directly. A low-level input to the DSIO pin issues a forced break to set the S1C processor into debug mode. Although this signal is pulled up through about 100k ohm internally, when not debugging, we recommend either removing the 33ohm resistor to reduce noise and other problems or pulling this line up to the VDD level.

8.3 Notes on Use of S5U1C17001H

Wiring between the S1C processor and target connector

When wiring the S1C processor to the target system connector for connecting the S5U1C17001H, insert a 33ohm resistor in series between the S1C processor DSIO pin and the connector. This resistor must be placed as close to the S1C processor as possible. If the reset line is not connected, the system can be operated without this 33ohm resistor. However, we recommend inserting this resistor to prevent malfunctions. The other pins are connected directly. The total length of the line must be under 5 cm. A low-level input to the DSIO pin issues a forced break to set the S1C processor into debug mode. Although this signal is pulled up through about 100k ohm internally, when not debugging, we recommend either removing the 33ohm resistor to reduce noise and other problems or pulling this line up to the VDD level.

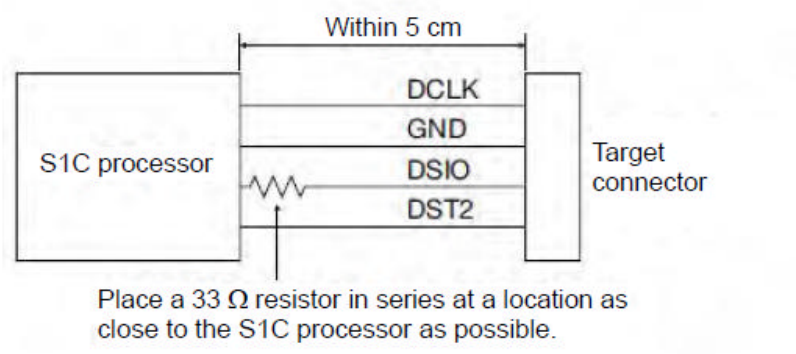

Figure 8.3.1 Wiring between S1C Processor and Target Connector

(Correct)

4.1.1 Target Interface Connector

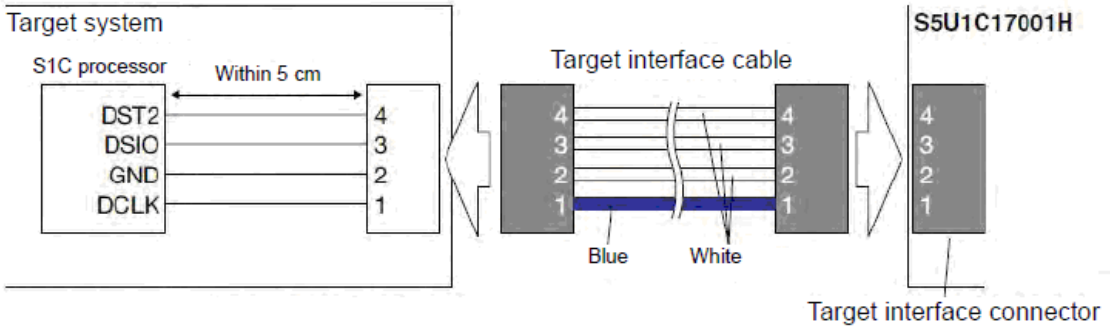

Figure 4.1.1.1 Connecting the Target System

Notes:

- Please refer to a technical manual of each model for the connection method with the target system.

8.3 Notes on Use of S5U1C17001H

Wiring between the S1C processor and target connector

Please refer to a technical manual of each model for the connection method with the target system. Moreover, The total length of the line must be under 5 cm.

(Delete Figure 8.3.1)

Oct/04/2013

errata_s5u1c17001h2_2

S1C17 Manual errata

ITEM:			
Object manuals	Document codes	Items	Pages
S5U1C17001H2 User Manual (ICDmini Ver2.0)	411153403	**8.3 Notes on Use of S5U1C17001H**	33

(Error)

There is no description.

(Correct)

About how to put this product

Please use this product with the rubber foot below.

About the accessory

Please use the USB cable attached to this product.

About the using environment requirements

Please use this product indoors.

Oct/04/2013

errata_s5u1c17001h2_3

S1C17 Manual errata

ITEM:

Object manuals	Document codes	Items	Pages
S5U1C17001H2 User Manual	411153403	7. Firmware Update	28

(Error)

Added.

(Correct)

(6) Check the current firmware version, and enter the following commands if updating is required.

When S1C17 processor is selected

(gdb) c17 fwe 0
(gdb) c17 fwe 1
(gdb) c17 firmupdate *path¥filename*.saf

When S1C33 processor is selected

(gdb) c33 firmupdate *path¥filename*.saf

path: Path for location of new firmware
filename.saf: File name for new firmware

The process is complete when the following is displayed after the commands have been entered.

When S1C17 processor is selected

(gdb) c17 fwe 0
Erase flash data …done
(gdb) c17 fwe 1
Erase flash erase/write program …done
(gdb) c17 firmupdate *path¥filename*.saf
Erase flash data ...done
Load flash data ....done
ICD firmware update ...done
Please quit gdb, and power off ICD when LED2 / LED3 / LED4 is green.
(LED is green in the case of SVT17701).

When S1C33 processor is selected

(gdb) c33 firmupdate *path¥filename*.saf
Erase flash data ...done
Load flash data ....done
ICD firmware update ...done
Please quit gdb, and power off ICD when OK LED is on.

This procedure may take about 15 minutes.
While firmware updating is in progress, do not reset the S5U1C17001H or turn off the power.
Otherwise it may not be possible to restart the S5U1C17001H.

S1C17 Manual errata

ITEM:			
Object manuals	Document codes	Items	Pages
S5U1C17001H2 User Manual (ICDmini Ver2.0)	411153403	4.1.2.1 Target reset signal output (TARGET RST OUT)	16

(Error) **4.1.2.1 Target reset signal output (TARGET RST OUT)**

When using the TARGET RST OUT output, a reset circuit as shown in the figure below is recommended.

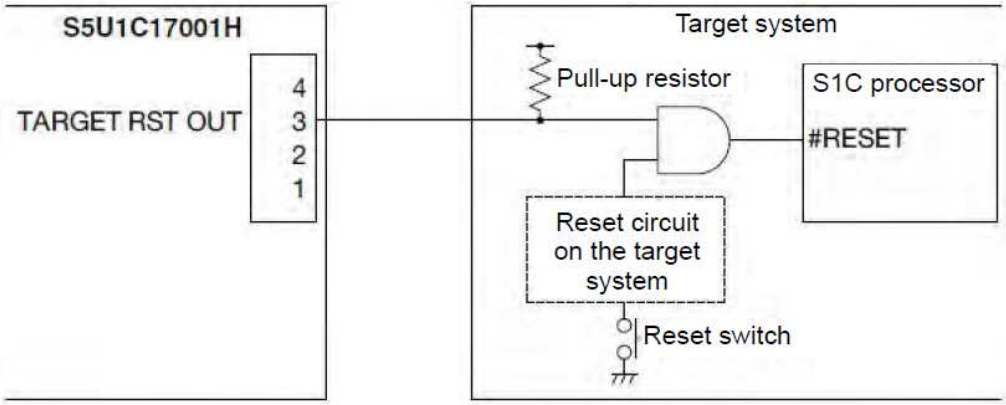

Figure 4.1.2.1 Example of Target Reset Circuit

(Correct) **4.1.2.1 Target reset signal output (TARGET RST OUT)**

The TARGET RST OUT terminal of S5U1C17001H2 is the following circuits.

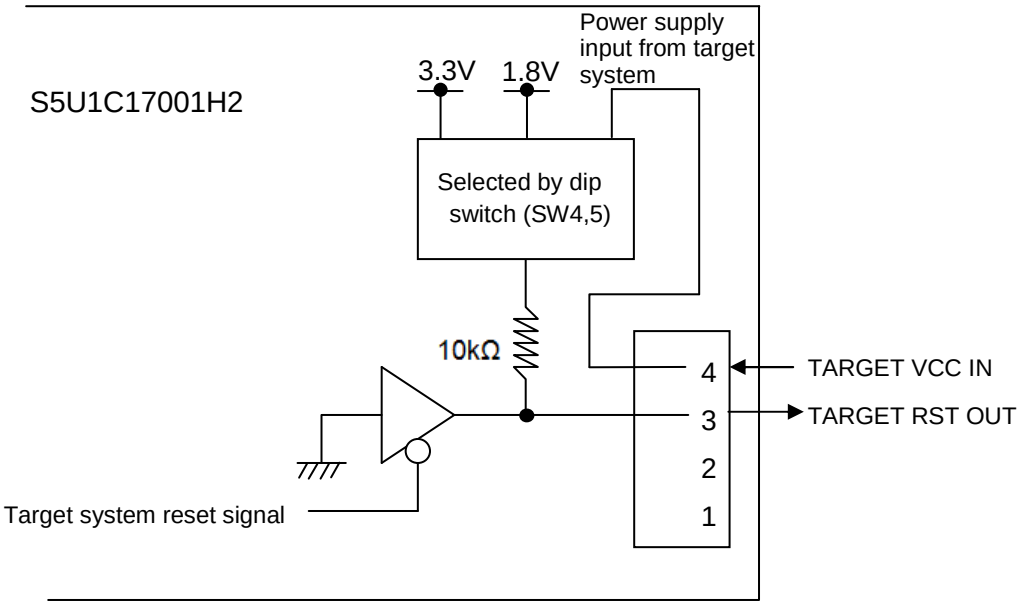

Figure 4.1. 2.1 Target reset terminal output circuit

The reset pulse is the following waveforms. Please note that the rising up waveform changes by the load situation of the target board.

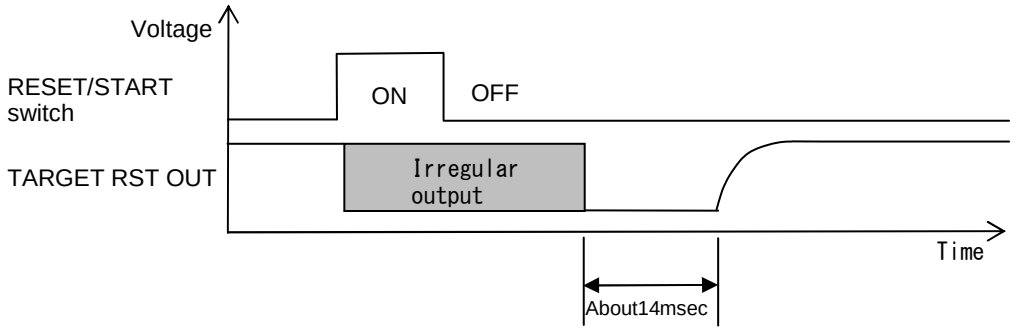

Figure 4.1.2.2(a) Reset terminal output waveform

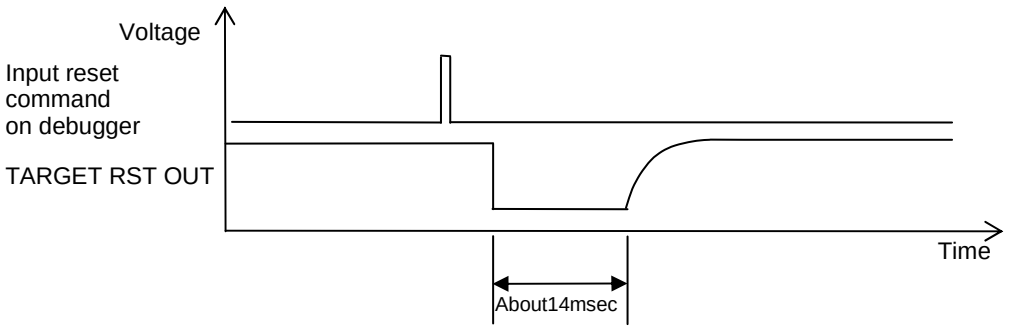

Figure 4.1.2.2(b) Reset terminal output waveform