Six Day Faculty Development Programme (FDP)
On
Integration of Renewable Energy Sources
(3rd – 8th June 2019)

Organized by

Electronics & ICT Academy, NIT Warangal

(Sponsored by Ministry of Electronics and Information Technology (MeitY), GOI)

Preamble:

"Electronics & ICT Academy" was established at NIT Warangal with financial assistance from MeitY, GoI. The role of this academy is to offer Faculty Development Programmes in emerging areas of Electronics, Information and Communication Technologies. The other activities of this academy include Training & Consultancy services for Industry, Curriculum Development for Industry, Continuing Education Programmes for working professionals, Advice and Support for technical incubation and entrepreneurial activities.

The role of Renewable Energy Sources (RES) need not be emphasized in the present scenario of dwindling resources of conventional energy and their harmful impact on the environment. Governments and industries are steadfastly promoting renewable energy technologies with an optimism of seeking solutions to these problems. Of the Renewable Energy Sources (RES), solar and wind energy are being employed for power generation. With the recent strides in technology, fuel cells are also promising. Distributed Generation with RES promises reliable power supply at a lower cost in the foreseeable future. Power electronic technology holds the key to achieve a successful integration of RES to the grid.

This course intends to provide an overview of power electronic interfaces and control algorithms for integrating RES to the grid. In addition, this course would also provide a basic hands-on training on the development of control schemes using modern tools such as Digital Signal Processors (DSPs) and Field Programmable Gate Arrays (FPGAs) for real-time implementation.

Major Course Contents:

- Design and Modeling of Photovoltaic, Wind & Fuel Cell System
- Power Electronic Interfaces for Renewable Energy Sources
- Control of Real and Reactive Power and Grid Interface
- Power Quality and Grid Standards
- Simulation of power electronic interfaces for PV & Fuel Cell systems
- Broad View of Digital Signal Processor (DSP) & Field Programmable Gate Array (FPGA) programming and Interface with Matlab for Real time workshop
- Hands on Training on advanced digital control platforms such as DSP & FPGA
- Demonstration of Power Converters

Eligibility:

The programme is open to the Faculty and Ph.D scholars of Electrical Engineering and allied disciplines. Industry personnel working in the concerned/allied discipline can also attend.

Faculty conducting this programme:

The programme will be conducted by the faculty members from **NIT Warangal**. Academicians in the concerned field from **IITs & NITs** are invited to deliver lectures in the programme. Speakers from industries are also expected to act as resource persons.

Registration Fee Particulars:

Faculty and Research Scholars	**Rs. 2500/-**
Faculty of SC/ST category	**Rs. 1875 (SC/ST participants should submit the copy of their caste certificate to claim the concession along with application form)**
Industry Participants	**Rs. 7500/-**

SC/ST concession is only for faculty of mentioned states.
Research Scholars are not eligible for SC/SC Concession.

The entire registration fee is to be collected in the form of online NEFT transfer using the following details:

Online Transfer Details
Account Name: Electronics & ICT Academy NITW
Account No: 62423775910
IFSC: SBIN0020149

Accommodation:

All the selected participants will be provided FREE boarding & lodging in the institute guest house. No TA will be paid for the participants.

How to apply:

A filled in form of application in the prescribed format duly signed and sponsored by appropriate authorities (along with payment details) should reach the coordinator by speed-post. It is also mandatory to send scanned application form and proof for Fee transfer details through e-mail to kiruba81@nitw.ac.in as selection will be intimated only through mail.

Selection Criteria:

Selection will be done based on first-cum-first-serve basis and the confirmed candidates will be notified immediately. The maximum number of participants will be **40 (Forty)**. Additionally 10 participants from industry are allowed to participate. The list of selected participants will be intimated through e-mail. In case a candidate is not selected, the FDP fee will be remitted back to the account from which it is received. A test will be conducted at the end of the course. Candidates will be issued satisfactory certificates on successful completion of the course. Reservations are followed for selecting candidates as per GoI norms.

Important dates:

Last date (Application & DD)	**20/05/2019**
Selection List by E-mail	**24/05/2019**
Duration of Program	**3/6/2019 - 8/6/2019**

About Institute, Department and Warangal:

NIT Warangal, formerly known as Regional Engineering College was established in 1959. Over the years it has developed into a premier institute of higher learning and is ranked among the top technical education institutions in India. There are 14 Departments offering eight undergraduate and 32 post-graduate programmes besides doctoral programmes. About 5000 students across the country and about 500 international students study on the campus.

The Department of Electrical Engineering was established as one of the major departments of NITW, in the year 1959. It offers B.Tech in Electrical & Electronics Engineering, M.Tech program in Power Electronics & Drives and Power Systems and Ph.D program. The Department has strong Industry interaction and is involved in various Research & Consultancy projects in coordination with industry, Governments of India, Telangana & Andhra Pradesh.

Warangal is known for its rich historical and cultural heritage. It is situated at a distance of 140 Km. from Hyderabad. Warangal is well connected by rail and road. National Institute of Technology campus is 2 Km. away from Kazipet junction and 12 Km. away from Warangal station.

Coordinators:

Dr. A. Kirubakaran, Assistant Professor
Dr. V.T. Somasekhar, Professor

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1. Name :

2. Designation :

3. Institution :

4. E-mail :

5. Mobile No. :

6. Payment Detail :
   Transaction No.: Amount:

   Bank: Date:

7. Address for Correspondence

8. Educational Qualifications with Specialization:

9. Subjects taught so far:

10. No. of Refresher Courses/Workshops attended:

11. Experience (in years)
    Teaching :
    Research :
    Industry :

12. Accommodation required: YES / NO

13. Are you belong to SC/ST: YES / NO
(If yes, please specify and attach a copy of caste certificate to claim the concession)

Declaration

The information provided is true to the best of my knowledge. If selected, I agree to abide by the rules and regulations of the FDP and shall attend the course for the entire duration. I also undertake the responsibility to inform the Coordinator in case, I am unable to attend the course.

Place:
Date: Signature of the applicant

SPONSORSHIP CERTIFICATE

Dr./Mr./Ms. ……..…… …… is an employee of our Institute/Organization and is hereby sponsored to participate in the FDP on **"Integration of Renewable Energy Sources"** sponsored by Electronics & ICT Academy during 3rd – 8th June, 2019 at National Institute of Technology, Warangal.

Place:

Date: Signature of Head of Institution

(With seal)

Address for correspondence:

Post your application form with proof of Fee Payment Details

Dr. A. Kirubakaran,
Assistant Professor,
Department of Electrical Engineering,
National Institute of Technology Warangal,
WARANGAL - 506 004, (T.S.), India.

E-Mail the scanned copies of filled-in and duly signed application form *along with proof for Fee Payment Details & date to* **kiruba81@nitw.ac.in**

For more details about Electronics & ICT Academy, NIT, Warangal, please visit: https://nitw.ac.in/eict/

For any enquiry contact:
Mobile: 09603722359
Land line: 0870-246 2243

Coordinators

Dr. A. Kirubakaran & Prof. V.T. Somasekhar
Department of Electrical Engineering
National Institute of Technology Warangal
WARANGAL – 506 004, (T.S.), INDIA.