

Easy Programming using WindLGC

Online test and simulation mode allows convenient programming

WindLGC Programming Software for IDEC SmartRelay

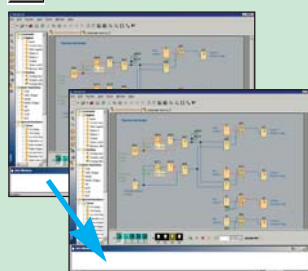
Enhanced functions in WindLGC such as offline simulation and online testing of programs greatly reduces programming time.

Online Test



Function Block
only!

Simulation

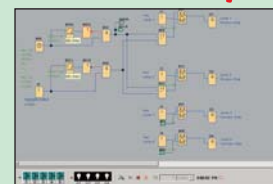


Wiring status and function block parameters can be confirmed by simulating on the WindLGC programming screen. Simulation is possible for every scan or for a predetermined period of time.

When WindLGC is connected to the IDEC SmartRelay installed in a control panel or equipment, the I/O statuses can be monitored on the WindLGC screen. Downloading user programs and testing operations greatly facilitate maintenance.

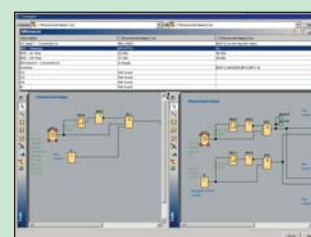
Set Output

New



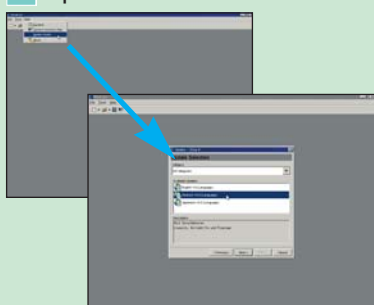
Digital outputs can be turned on or off independently. Useful for confirming operation of subsequent programs.

Compare



When adding or modifying a part of the user program, this function makes it possible to compare two programs on the WindLGC screen.

Update Center

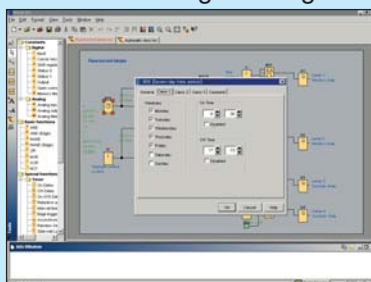


Free upgrade from WindLGC Ver. 3 can be downloaded from the update center or IDEC website through the Internet.

<http://www.idec.com/english/download/>

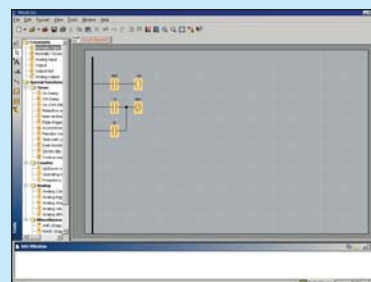
Create function block and ladder diagrams using simple drag and drop functions

Co GF SF Function Block Programming



Function block parameters are entered and modified in the function block dialog boxes.

Ladder Programming



In addition to function block diagrams, WindLGC can program ladder diagrams for the IDEC SmartRelay.

When downloading the user program to the base module, ladder diagrams are converted into function block diagrams.

For details, see the user's manual and FAQ available at the following website.

<http://www.idec.com/faq/en/controller/>

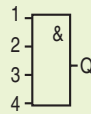
WindLGC Ver. 5 System Requirements

CPU	Pentium III 500 MHz or higher
OS	Windows XP/2000/98/95/Me/NT4.0
Hard disk space	90 MB
RAM	128 MB
Display	800 × 600 pixels, 256 colors (1024 × 768 recommended)

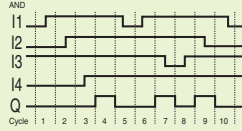
GF General Function Blocks

• AND

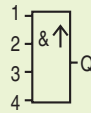
Series connection of normally open contacts



• AND (Edge)

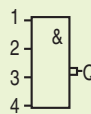


Edge detection with edge evaluation (pos. edge)

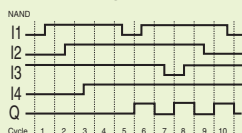


• NAND

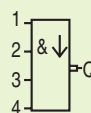
Parallel connection of normally closed contacts



• NAND (Edge)

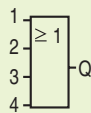


Edge detection with edge evaluation (pos. edge)



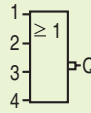
• OR

Parallel connection of normally open contacts



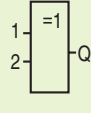
• NOR

Series connection of normally closed contacts



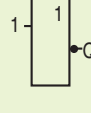
• XOR

Double changeover contact



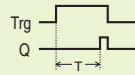
• NOT

Connection of closed contact

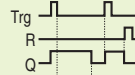


SF Special Function Blocks

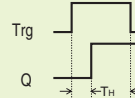
• On-delay



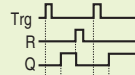
• Off-delay



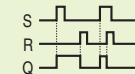
• On-/Off-delay



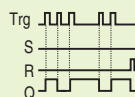
• Retentive on-delay



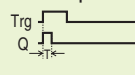
• Latching relay



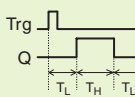
• Current impulse relay



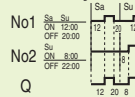
• Interval time-delay relay/ Pulse output



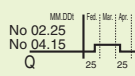
• Edge-triggered interval time-delay relay



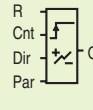
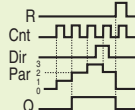
• Seven-day time switch



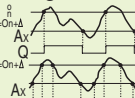
• Twelve-month time switch



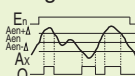
• Up/down counter



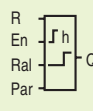
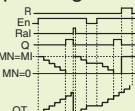
• Analog differential trigger



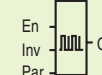
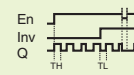
• Analog value monitoring



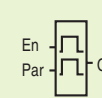
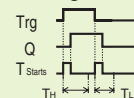
• Operating hours counter



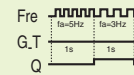
• Asynchronous pulse generator



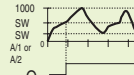
• Random generator



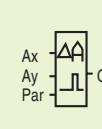
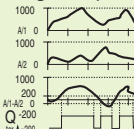
• Frequency trigger



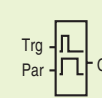
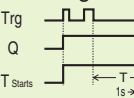
• Analog trigger



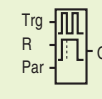
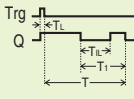
• Analog comparator



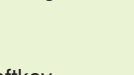
• Stairwell light switch



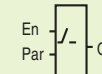
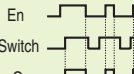
• Dual-function switch



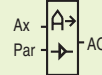
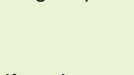
• Message texts



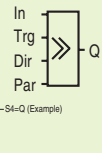
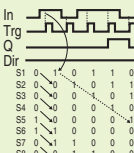
• Softkey



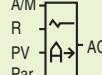
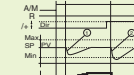
• Analog amplifier



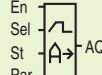
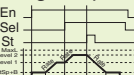
• Shift register



New PI controller



New Analog ramp control



New Analog multiplexer

