Study on electrical properties of Ni-doped SrTiO₃ ceramics using impedance spectroscopy

S K ROUT*, S PANIGRAHI and J BERA†

Department of Physics, †Department of Ceramic Engineering, National Institute of Technology, Rourkela 769 008, India

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Abstract. The ceramics, $SrTiO_3$ (ST) and 0.4, 0.8 atom% Ni doped $SrTiO_3$, were prepared by solid state reaction route. The average grain size of undoped and doped samples was measured and found to be 1.2, 1.9 and 3.7 µm, respectively. The impedance measurements were conducted at $400-600^{\circ}C$ to separate grain and grain boundary contributions. The grain and grain boundaries relaxation frequencies were shifted to higher frequency with temperature. Bulk resistance of doped and undoped ST ceramics was more or less the same. Single grain boundary resistance of doped sample was higher than that of undoped one, indicating that GB resistance increases with acceptor doping. Activation energies were calculated to confirm the same.

Keywords. Ni doped SrTiO₃; impedance spectroscopy; grain; grain boundary; acceptor.

1. Introduction

In commercial multilayer ceramic capacitors (MLCs), perovskite structure titanate are frequently used as high permittivity dielectrics. Alkaline earth titanates like BaTiO₃, SrTiO₃ etc are widely used in microelectronic devices due to their excellent dielectric properties and high thermal stability. SrTiO₃, a technologically highly important class of perovskite material, is used in PTC resistors, capacitors and sensors (Daniels *et al* 1978; Gerblinger and Meixher 1991). Important applications of SrTiO₃ are as substrates for the heteroepitaxial growth of high T_c superconductors (Shlom *et al* 1990; Kawai *et al* 1991), in multi-layer capacitors (Dedyk *et al* 1998) and DRAM devices (Sakuma *et al* 1990) etc.

Ceramic oxide-based materials like SrTiO₃ contain electro-active intragranular (bulk) and intergranular (grain boundary) regions, whose properties depend on the close control of microstructure–stoichiometric relationships. Grain boundaries play an important role in the electrical properties of a variety of ceramic materials and components. In a broad range of ceramics, this shows ionic conduction, mixed ionic–electronic conduction or electronic conduction, grain boundaries act as barriers for the cross transport of the charge carriers. Often, the barrier character of the grain boundary is especially pronounced in the low temperature regime. In this regime, perovskite type titanates such as BaTiO₃, SrTiO₃ etc are employed as high permittivity dielectrics for capacitor applications. At elevated temperatures (i.e. well above the Curie tempera-

Acceptor, e.g. Ni-doped SrTiO₃, is a mixed conductor of oxygen vacancies and electron holes. The grain boundary properties of acceptor doped SrTiO₃ have already been investigated by impedance spectroscopy on polycrystals and bicrystals (Denk *et al* 1997), in time dependent voltage stapes measurements (Volmann *et al* 1997), by simulations (Hagenbeck and Waser 1998), using micro-contact measurements (Rodewald *et al* 2001) etc.

Acceptor doped SrTiO₃ is again selected as our main material because it shows a defect and crystal structure very similar to BaTiO₃, which still is the most important ceramic in the ceramic industries. In contrast to BaTiO₃, SrTiO₃ is not ferroelectric over the complete temperature range of technical interest. For our purpose, this is beneficial since the ferroelectric behaviour superimposes an additional degree of complexity by significantly affecting the polarization and charge transport in the material. Acceptor doped SrTiO₃ which is the core of the present pa-

ture), the BaTiO₃ based ceramics gave clear evidence for GB layers of high resistance, which is interpreted as a Schottky-type depletion space charge effect. For most of the above applications, the titanates must be net acceptor doped to prevent semiconduction in the ceramics (Hagemann *et al* 1983/84). The high insulation resistance of MLC components is caused mainly by the fact that grain boundaries (GBs) in the dielectric ceramic act as high resistive barriers for the cross transport of charge carriers. Often, under high temperatures and high voltage fields, these resistive grain boundary barriers are reduced and that give rise to substantially field-enhanced leakage currents through the components. That is the reason why detailed dielectric studies, especially at GB barrier interface are important for the perovskite-type titanates.

^{*}Author for correspondence (skrout@nitrkl.ac.in)

per aims to describe the effect of acceptor concentration on microstructure and electrical properties of the ceramic at elevated temperatures.

2. Experimental

Undoped and nickel-doped (0.4 and 0.8 atom%) strontium titanate were prepared by solid-state reactions from strontium carbonate (S.D. Fine Chem, Mumbai), titanium dioxide (E. Merck India Ltd.) and nickel nitrate (S.D. Fine Chem, Mumbai). All the powders had 99% purity. The powders were mixed in agate mortar using isopropyl alcohol up to dryness. Mixed powder was calcined at 1200°C for 1 h and then milled again to destroy agglomerates. The calcined powder was characterized by XRD and showed a perovskite structure without evidence of additional phases. For electrical property measurements, the disks were pressed uniaxially at 200 MPa with 2 wt% PVA solution added as binder and then they were sintered at 1300°C for 4 h. All the samples were of uniform thickness (~ 1.68 mm) and diameter (~ 10.4 mm). Disk density, estimated approximately from its external dimension using Archimedes's principle, was ~ 99% of theoretical density. Microstructures were taken by optical microscope (figure 2). Typical average grain sizes were estimated for undoped and doped samples using optical microscope. Silver electrodes were printed on to opposite disk faces and were sintered at 700°C for 15 min. Impedance measurements were carried out over a range 10 Hz-13 MHz using HP-4192A LF impedance analyser, connected with a PC in the temperature range, 25-600°C.

3. Results and discussion

Figure 1 shows the XRD patterns of the Ni doped and undoped samples. The figure indicates all the samples are of single phase. The patterns were indexed in cubic structure with ccp14 powder indexing program, check cell. The

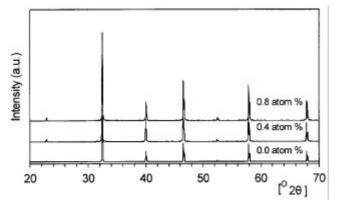
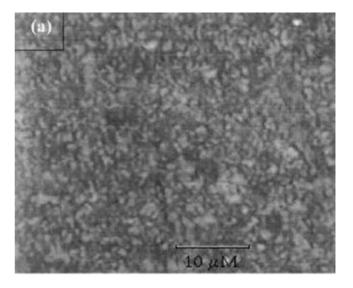
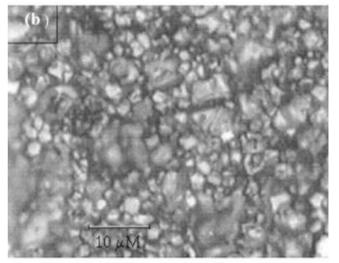


Figure 1. XRD patterns of the undoped, 0.4 atom% Ni doped and 0.8 atom% Ni doped SrTiO₃.





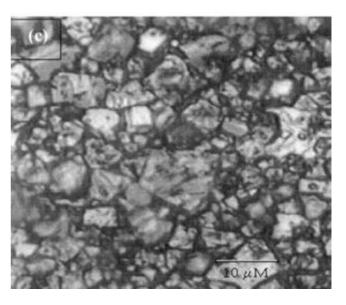


Figure 2. Microstructure of $SrTiO_3$ samples fired at $1300^{\circ}C/4$ h: (a) undoped, (b) 0.4 atom% Ni doped and (c) 0.8 atom% Ni doped.

lattice parameter was, a = 3.8995(2) Å, in good agreement with that of JCPDS-Card no. 35-734 (a = 3.9050 Å).

Figure 2 shows the optical micrograph of $SrTiO_3$ samples. The average grain sizes are $1\cdot 2$, $1\cdot 9$ and $3\cdot 7$ µm, for undoped, $0\cdot 4$ atom% and $0\cdot 8$ atom% Ni-doped samples, respectively. The samples were fired in same firing schedule. Then the differences in grain size may be attributed to the effect of Ni-doping. NiO may enhance grain growth at the experimental sintering temperature. This may be caused due to the nucleating behaviour of Ni in the $SrTiO_3$ ceramic.

Grain boundaries act as barriers for the cross transport of the charge carriers. Often, under high temperature and high voltage fields, this grain boundary barriers are reduced, which gives rise to a substantially field enhanced

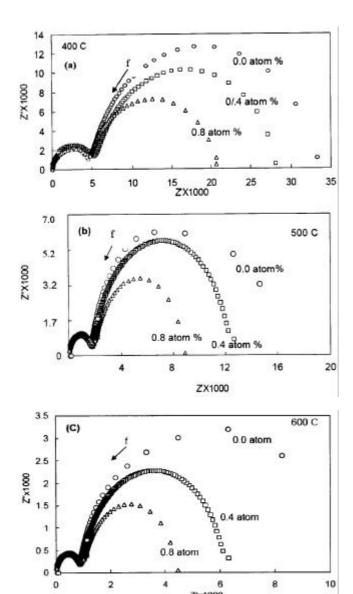


Figure 3. Impedance spectra of the samples at different temperatures ((O) 0.0 atom%, (\square) 0.4 atom% and (Δ) 0.8 atom% Ni-doped).

leakage currents through the components. So a detailed dielectric study, especially at grain boundary barriers, is important for the perovskite type titanates. It is well known that acceptor doped SrTiO₃ has positively charged GB interface, which gives rise to an electrostatic repulsion of the positively charged mobile oxygen vacancy at both sides of the boundary.

It is known that overall grain boundary resistance, $R_{\rm gb}$, increases with the decrease in grain size due to the increase in number of boundaries per unit thickness. A typical grain-boundary thickness of nickel-doped materials was estimated to be of the order of 100 nm (Volmann et al 1997), which is usually much lower than the average grain size. Hence, no significant changes are expected in bulk resistance on changing the grain size.

Figure 3 shows the impedance spectra of the samples at three representative temperatures. The amplitude of high frequency arc of the impedance spectra for three samples having different grain sizes does not vary significantly with increasing grain size (table 1) and is ascribed to the bulk resistance (R_b). Low frequency arc was not found at $< 400^{\circ}\text{C}$ for the three samples, which may be due to the effect of electrode relaxation process overlapping with the grain boundary relaxation process. However, with increase in temperature ($> 400^{\circ}\text{C}$) the low frequency arc was found as shown in figure 3. The low frequency arc is ascribed to $R_{\rm gb}$ because arc amplitude decreases with increase in temperature.

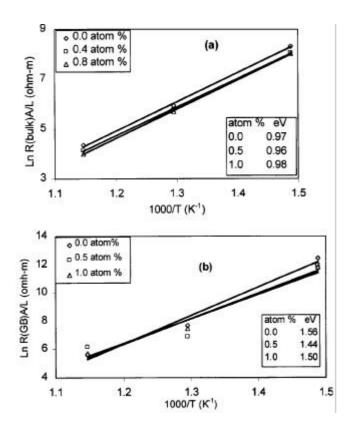
Figure 3 shows two semicircles (arc) for all samples representing two RC elements. The high frequency arc corresponds to R_bC_b response and low frequency arc corresponds to $R_{\rm gb}C_{\rm gb}$ response. The spectrum clearly indicates a combination of bulk and grain boundary impedances connected in series. The impedance for this circuit, $Z^* =$ $[R_{\rm b}^{-1} + j w C_{\rm b}]^{-1} + [R_{\rm gb}^{-1} + j w C_{\rm gb}]^{-1}$, where the subscripts b and gb refer to bulk and grain boundary. Grain and grain boundary relaxation frequencies for three different samples are tabulated in table 1. Both the grain and grain boundary relaxation frequencies increase with increase in dopant concentration, which may be due to increase in average grain size. It is well known that R_{gb} increases with decrease in grain size due to the increase in number of boundaries per unit thickness (Jurado et al 2000). As in the case of samples of unequal grain thickness, to compare the actual grain boundary resistivity, grain size contribution is considered by the relation,

$$\mathbf{r}_{\rm gb} = d_{\rm g}AR_{\rm gb}/d_{\rm gb}L$$
,

where $d_{\rm gb}$ is the thickness of the grain boundary layer, $d_{\rm g}$ the average grain size, L the sample thickness, A the electrode area and $R_{\rm gb}$ the grain boundary resistance. Volmann and Waser (1994) studied a typical grain boundary thickness of Ni doped materials which was estimated to be of the order of 100 nm, which is usually much less than the average grain size. $R_{\rm b}$ of all three samples are more or less same at these representative temperatures

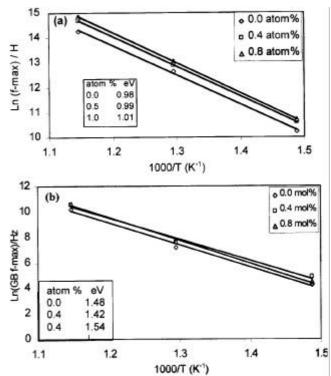
Table 1. Fitting parameter and activation energy obtained from impedance spectra (figure 3)	Table 1.	Fitting parameter and	l activation energy	obtained from	impedance spectr	a (figure 3).
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Sample (Av. grain size)	Parameter	400°C	500°C	600°C	Activation energy (eV)
Undoped (1·2 μm)	$R_{\rm b}({\rm k}\Omega)$	5	2	1	0.96 (B)
• • •	f _{max} (bulk)/kHz	27.8	310	1550	0.98 (B)
	$R_{\rm gb}({\rm k}\Omega)$	29	16	9	1.51 (GB)
	$f_{\rm max}$ (GB)/kHz	0.12	4.8	39.8	1.48 (GB)
0·4 atom% Ni doped (1·9 μm)	$R_{\rm b}$ (k Ω)	5	2	1	0.97 (B)
• • •	f _{max} (bulk)/kHz	40	400	2400	0.99 (B)
	$R_{\rm gh}$ (k Ω)	23	10	5.2	1.5 (GB)
	$f_{\rm max}$ (GB)/kHz	0.31	6.8	90.01	1.42 (GB)
0·8 atom% Ni doped (3·7 μm)	$R_{\rm b}({ m k}\Omega)$	5	2	1	0.98 (B)
, and the second	f _{max} (bulk)/kHz	45	470	2700	1·01 (B)
	$R_{\rm gh} ({ m k}\Omega)$	16	7	3.5	1.51 (GB)
	$f_{\rm max}$ (GB)/kHz	0.53	9.7	230.01	1.52 (GB)



Figures 4(a)-(b). Temperature dependence of bulk and grain boundary resistivity obtained from impedance data.

due to same type of bulk grain behaviour. Hence, no significant changes are expected in the bulk resistance on changing the average grain size. In our case A and L are same for different samples. $d_{\rm gb}$ is 100 nm for Ni doped materials, as suggested by Volmann and Waser (1994). So to compare the resistivity, only average grain size should be multiplied with the observed resistance value (table 1) from the impedance spectra, i.e. the relation $(\mathbf{r}_{\rm gb} = d_{\rm g}AR_{\rm gb}/d_{\rm gb}L)$ can be considered as grain size $\times R_{\rm gb}$.



Figures 5(a)–(b). Temperature dependence of bulk and grain boundary resistivity obtained from respective relaxation frequencies.

Hence, using this relation it could be clearly observed that $r_{\rm gb}$ (undoped) $< r_{\rm gb}$ (0·4 atom% doped) $< r_{\rm gb}$ (0·8 atom% doped). This is due to the formation of highly resistive grain boundary due to Ni doping. Again it can be clearly seen that $r_{\rm gb}$ (600°C) $< r_{\rm gb}$ (500°C) $< r_{\rm gb}$ (400°C) for all samples due to the increase in leakage current at the grain boundary.

Figures 4(a) and (b) show the temperature dependence of bulk resistivity and grain boundary resistivity, respec-

tively obtained from the impedance data. Bulk resistance follows generic Arrhenius law, giving an activation energy of about ~ 1 eV (figure 4(a)), which is close to the value reported by Volmann *et al* (1997). Again grain boundary activation energy is calculated and observed to be about 1.5 eV for 0.8 atom% doped sample and 1.56 eV for undoped sample which are slightly lower than the w type conductivity profile (~ 1.6 eV) reported for polycrystalline Ni doped samples (Volmann *et al* 1997), but for activation energy of 0.4 atom% doped sample, it was 1.44 eV which is lower than the w type conductivity and this may be due to very small and non-uniform grain size.

Figures 5(a) and (b) show the temperature dependence of bulk resistivity and grain boundary resistivity, respectively obtained from the relaxation frequency. Here the bulk resistance follows the generic Arrhenius law giving activation energy very close to 1 eV as reported earlier (Volmann *et al* 1997). Again grain boundary activation energy obtained from figure 5(b) more or less agreed with the values that were calculated from resistance data (figure 4(b)).

4. Conclusions

Ni doped sample showed higher resistivity than undoped sample and that may be explained by the formation of thicker space charge layer at the interface due to higher concentration of acceptor doping. Fine-grained ST ceramics are desirable for higher GB resistivity.

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