Getting Started with AURIXTM Development Studio Installation and first steps

AURIX™ Development Studio Training V1.0.4





Scope of work

This tutorial provides a guide for the user to:

- Install AURIX™ Development Studio V1.3.0
- Create new project
- Import project (Infineon Code Examples Repository)
- > Build project
- Debug project
- Additional material



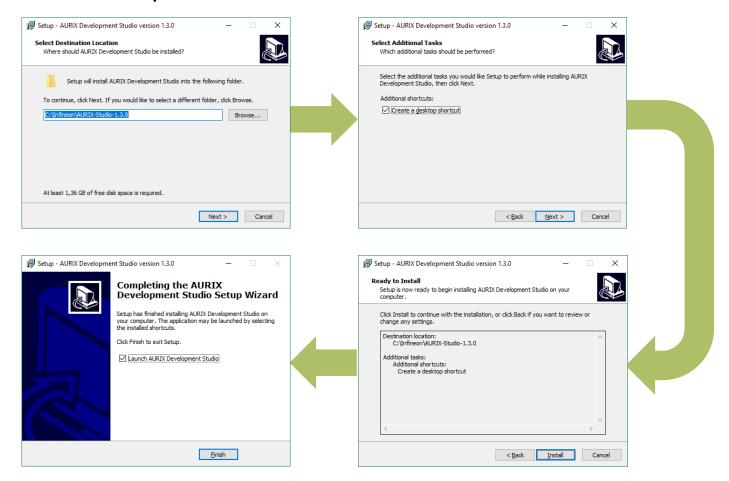
Download

The installation package of AURIX™ Development Studio can be found here: https://www.infineon.com/aurixdevelopmentstudio



Install AURIX™ Development Studio - 1

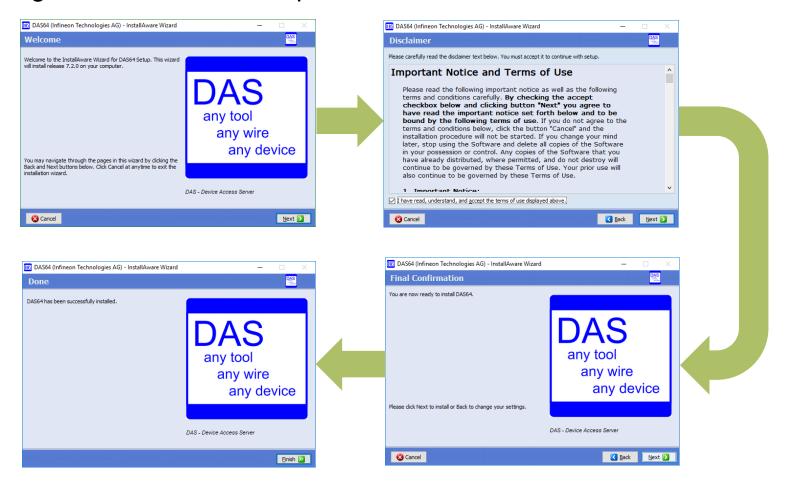
To install AURIX™ Development Studio, launch the installation package and follow the steps:





Install AURIX™ Development Studio - 2

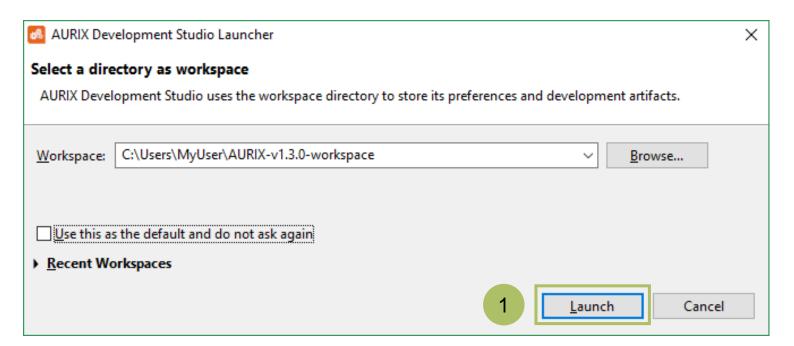
If DAS64 is not installed or outdated, it will be installed automatically during the AURIX™ Development Studio installation:





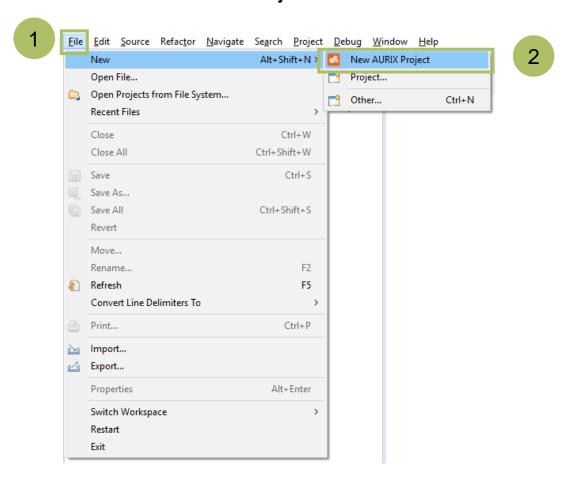
Workspace definition

After launching the AURIX™ Development Studio, it is necessary to select a workspace.



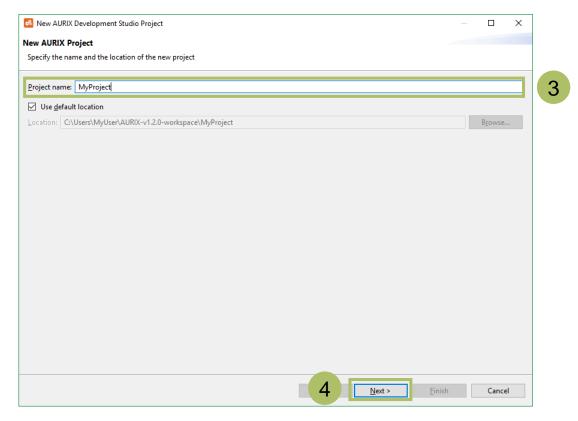


Once the program is started, a new project can be created by selecting File >> New >> "New AURIX™ Project".



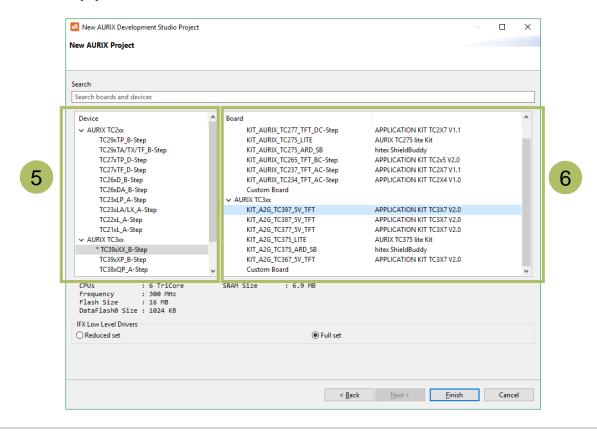


- > From the "New AURIX™ Development Studio Project" window, choose a name for the new project (3).
- The "Use default location" checkbox should be set in order to create the project inside the current selected workspace.



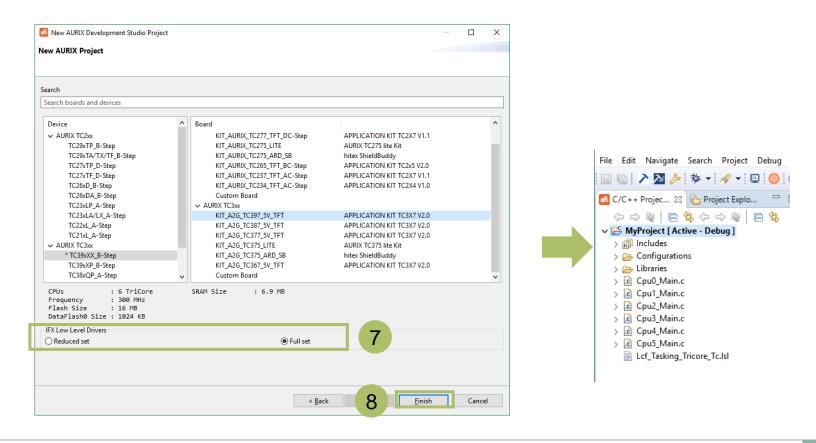


From the "New AURIX™ Development Studio Project" window, choose the device or the board. A specific device (5) or board (6) can be chosen from the left or right list. Furthermore, while selecting a board, the tool highlights the supported devices for that board and vice versa.





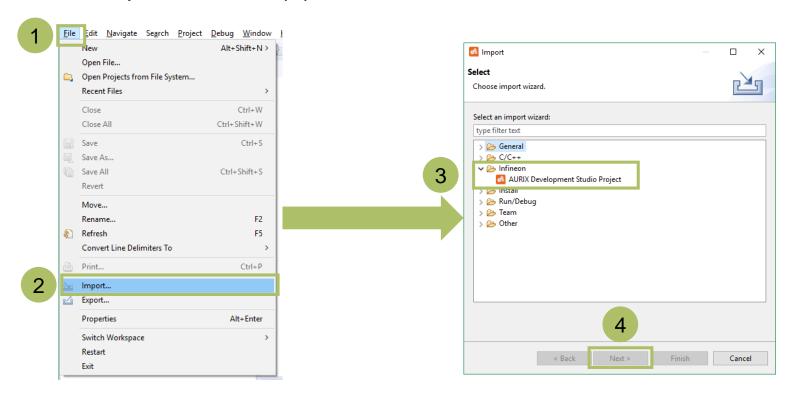
- Depending on the complexity of the project, the reduced or full set of drivers can be imported (7).
- By pressing "Finish" (8), a new project is created.





Import project (Infineon Code Examples Repository) - 1

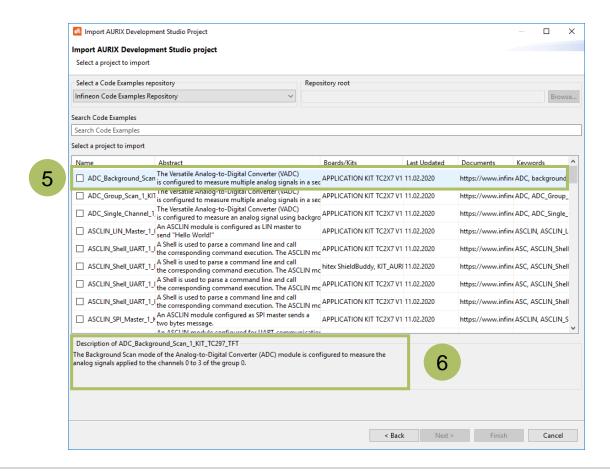
- Alternatively, it is possible to import an example project using File >> "Import..." utility (1-2) and selecting Infineon >> "AURIX™ Development Studio Project" type (3).
- At the end, press "Next" (4).





Import project (Infineon Code Examples Repository) - 2

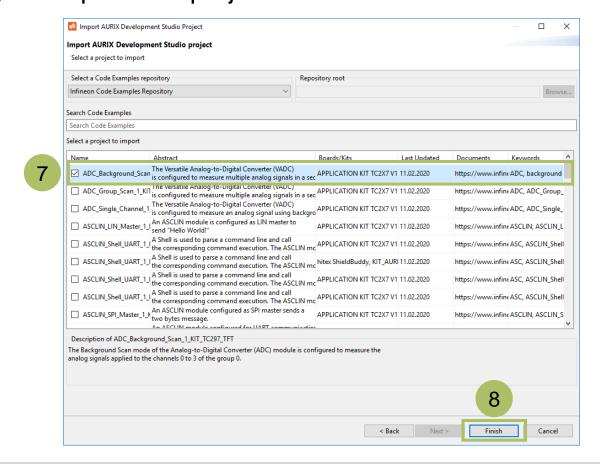
Hint: Clicking on an example project (5) in the list shows the example description (6).





Import project (Infineon Code Examples Repository) - 3

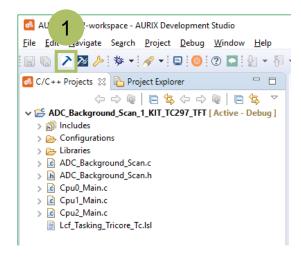
Select (double-click) an example project (7) from the list and press "Finish" (8). This creates a local copy of the example in your workspace directory and opens the project.

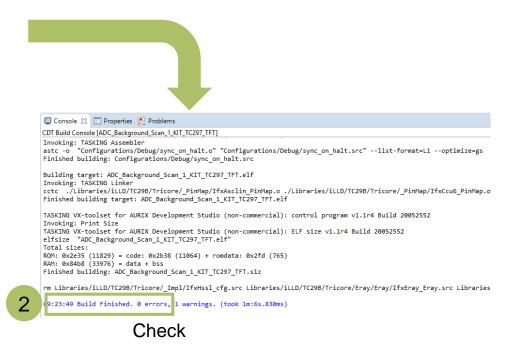




Build project

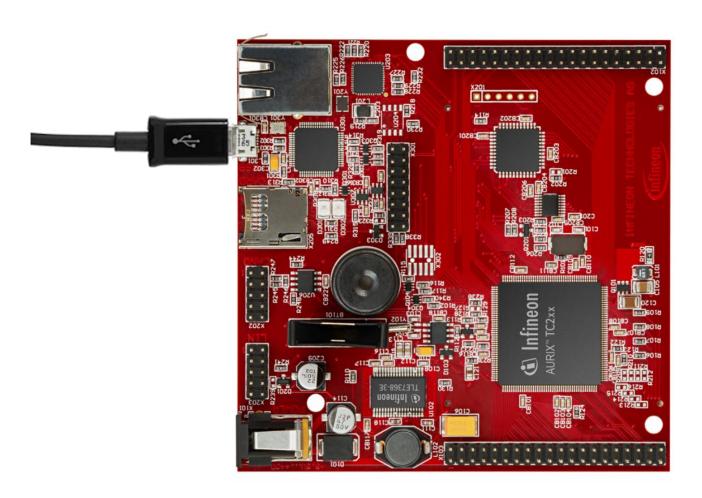
 Before debugging, it is necessary to build the project. Press the "Build Active Project" icon (1) and when the build is finished, check that there are no compiling errors (2).





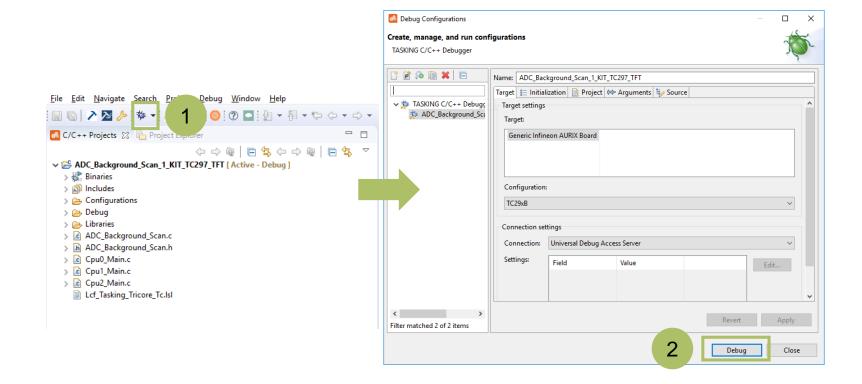


Connect your device via an USB cable to the PC.



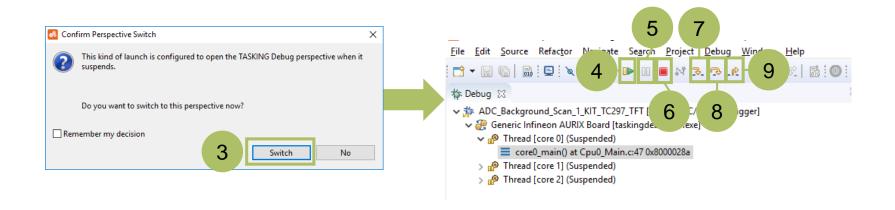


 In order to flash and debug the code, press the "Debug Active Project" icon (1) and then the "Debug" button on the "Debug Configurations" window (2).



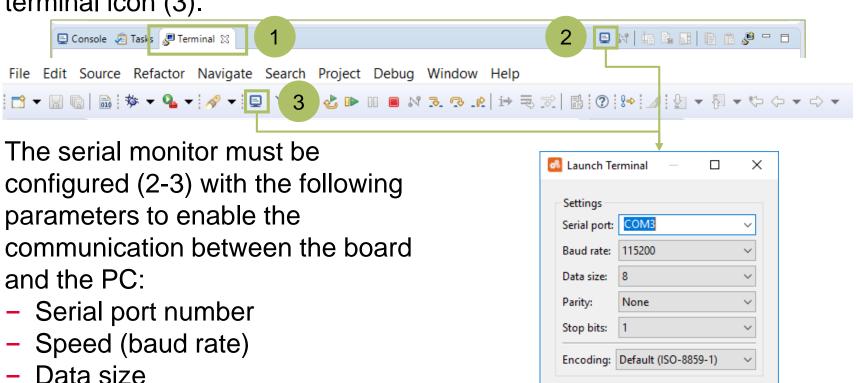


- Switch the perspective (3) and press "Resume" (4) to run the code.
- While running, the code can be stopped with the "Suspend" button (5).
- To terminate the debug session, press the "Terminate" button (6).
- Additionally, in the Debug perspective, it is also possible to run the code in single or multiple steps with the buttons "Step Into" (7), "Step Over" (8) and "Step Return" (9).





A serial monitor is open by default (1) in the Debug Perspective inside the AURIX™ Development Studio, or it can be open manually from the terminal icon (3).



Parity

Stop bits

?

OK

Cancel



Additional material - 1

- All the imported examples from Infineon come with a tutorial explaining the needed HW/SW setup, the code and how to run and test the example.
- The tutorial is accessible from the AURIX™ Development Studio by Ctrl + click on the link (1) in the Cpu0_main.c file.

```
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22 * WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, TITLE AND NON-INFRINGEMENT. IN NO EVENT SHALL THE
 23 * COPYRIGHT HOLDERS OR ANYONE DISTRIBUTING THE SOFTWARE BE LIABLE FOR ANY DAMAGES OR OTHER LIABILITY, WHETHER IN
 24 * CONTRACT, TORT OR OTHERWISE, ARISING FROM, OUT OF OR IN CONNECTION WITH THE SOFTWARE OR THE USE OR OTHER DEALINGS
25 * IN THE SOFTWARE.
 27⊖ /*\title ADC background scan source
 28 * \abstract The Versatile Analog-to-Digital Converter (VADC) is configured to measure multiple analog signals in a sequence using background scan request.
 29 * \description The Background Scan mode of the Analog-to-Digital Converter (ADC) module is configured to measure the
                    analog signals applied to the channels 0 to 3 of the group 0.
 31 *
 32 * \name ADC_Background_Scan_1_KIT_TC297_TFT
33 * \version V1.0.0
34 * \board APPLICATION KIT TC2X7 V1.1, KIT AURIX TC297 TFT BC-Step, TC29xTA/TX BC-step
 35 * \keywords ADC hackground scan conversion VADC ADC Background Scan 1 AURTY
 36 * \documents https://www.infineon.com/aurix-expert-training/Infineon-AURIX ADC Background Scan 1 KIT TC297 TFT-TR-v01 00 00-EN.pdf
 37 * \documents nccps.//www.infineon.com/aurix-experc-craining/rczsb_iccb_ori_r & _i i w.chm
 38 * \lastUpdated 2020-02-11
 40 #include "Ifx Types.h"
 41 #include "IfxCpu.h"
 42 #include "IfxScuWdt.h"
 43 #include "ADC Background Scan.h"
 45 IfxCpu_syncEvent g_cpuSyncEvent = 0;
47⊖ int core0_main(void)
 48 {
 49
        IfxCpu enableInterrupts();
 50
 51⊝
        /* !!WATCHDOGO AND SAFETY WATCHDOG ARE DISABLED HERE!!
 52
         * Enable the watchdogs and service them periodically if it is required
 53
 54
55
        IfxScuWdt disableCpuWatchdog(IfxScuWdt getCpuWatchdogPassword());
        IfxScuWdt disableSafetyWatchdog(IfxScuWdt getSafetyWatchdogPassword());
         /* Wait for CPU sync event */
        IfxCpu_emitEvent(&g_cpuSyncEvent);
```



Additional material - 2

> From the same Cpu0_main.c file, it is possible to download the Infineon Low Level Drivers documentation (2) for the specific device used in the example.

```
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 22 * WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, TITLE AND NON-INFRINGEMENT. IN NO EVENT SHALL THE
 23 * COPYRIGHT HOLDERS OR ANYONE DISTRIBUTING THE SOFTWARE BE LIABLE FOR ANY DAMAGES OR OTHER LIABILITY, WHETHER IN
 24 * CONTRACT, TORT OR OTHERWISE, ARISING FROM, OUT OF OR IN CONNECTION WITH THE SOFTWARE OR THE USE OR OTHER DEALINGS
 25 * IN THE SOFTWARE.
 27⊖ /*\title ADC background scan source
 28 * \abstract The Versatile Analog-to-Digital Converter (VADC) is configured to measure multiple analog signals in a sequence using background scan request.
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33 * \version V1.0.0
 34 * \board APPLICATION KIT TC2X7 V1.1, KIT AURIX TC297 TFT BC-Step, TC29xTA/TX BC-step
 35 * \keywords ADC, background scan, conversion, VADC, ADC_Background_Scan_1, AURIX
 36 * \documents_https://www.infineon.com/aurix-expert-training/Infineon-AURIX_ADC_Background

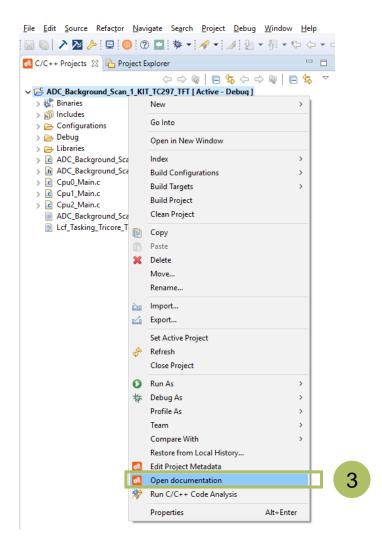
▼ TC297 TFT-TR-v01 00 00-EN.pdf

 37 * \documents https://www.infineon.com/aurix-expert-training/TC29B iLLD UM 1 0 1 11 0.chm
    * \lastUpdated 2020 02 11
    *****************
 40 #include "Ifx Types.h"
 41 #include "IfxCpu.h"
 42 #include "IfxScuWdt.h"
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 50
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        /* !!WATCHDOGO AND SAFETY WATCHDOG ARE DISABLED HERE!!
 52
         * Enable the watchdogs and service them periodically if it is required
 53
        IfxScuWdt disableCpuWatchdog(IfxScuWdt getCpuWatchdogPassword());
 55
        IfxScuWdt disableSafetyWatchdog(IfxScuWdt getSafetyWatchdogPassword());
         /* Wait for CPU sync event */
        IfxCpu_emitEvent(&g_cpuSyncEvent);
```



Additional material - 3

Hint: both the example's tutorial and the iLLD documentation can be opened by Right clicking on the project name and pressing the "Open documentation" utility (3).



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Document reference Getting_Started_with_AURIX™_ Development_Studio

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